

ASI-T-700MAKFN/A

Item	Specification	Unit
LCD Type	IPS TFT / Transmissive / Normally black	/
Size	7.0	Inch
Viewing Direction	Full viewing angle	O'clock
Gray Scale Inversion Direction	-	O'clock
$LCM (W \times H \times D)$	$165.00 \times 104.00 \times 8.40 (Max.)$	mm³
Active Area (W × H)	152.40 × 91.44	mm²
Pixel Pitch	0.1905×0.1905	mm²
Number of Dots	800 (RGB) × 480	/
Driver IC	HX8262+HX8643	/
Backlight Type	28LED	/
Interface Type	24-bit parallel RGB	/
Color Depth	16.7M	
Pixel Configuration	R.G.B Vertical Stripe	/
Top Polarizer Surface Treatment	Anti-glare	/
Input Voltage	3.3	V
With / Without TSP	Without TP	/
TP Surface Treatment	-	/
Weight	165	g



■ REVISION RECORD

REV NO.	REV DATE	CONTENTS	REVISED PAGE NO.
1.0	2019-09-18	First Release	



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■ GENERAL INFORMATION

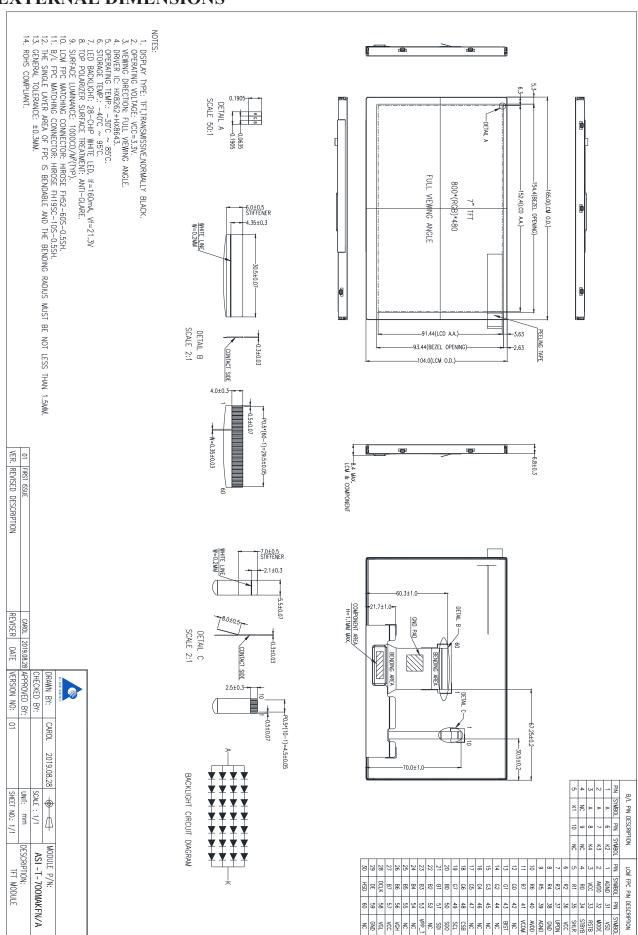
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Pixel Configuration	R.G.B Vertical Stripe	/
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Input Voltage	3.3	V
With / Without TSP	Without TP	/
TP Surface Treatment	-	/
Weight	165	g

Note 1: ROHS compliant;

Note 2: LCM weight tolerance: $\pm 5\%$.



■ EXTERNAL DIMENSIONS





■ ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min.	Max.	Unit	Note
	VCC	-0.3	4.5	V	
	VGH	-0.3	VGL+35.0	V	GND=0
Power Supply Voltage	VGL	-15	0.3	V	
	AVDD	-0.3	14.5	V	AGND=0
	VCOM	4.0	6.0	V	
Logic Signal Input Level	VIN	-0.3	4.5	V	
Operating Temperature	Тор	-30	85	°C	
Storage Temperature	Тѕт	-40	95	°C	

Note 1: Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

■ ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
	VCC	3.0	3.3	3.6	V	
	VGH	-	19	-	V	
Power Supply Voltage	VGL	-	-10	-	V	
	AVDD	-	12.4	-	V	
	VCOM	-	5.5	-	V	
Innat Cional Valtage	VIH	0.7VCC	-	VCC	V	Note 1
Input Signal Voltage	VIL	0	-	0.3VCC	V	
	Ivcc	-	15	-	mA	VCC=3.3V, white pattern
Current of Power Supply	Iavdd	-	20	-	mA	AVDD=12.5V, white pattern
	Ivgh	-	0.25	-	mA	VGH=19V, white pattern
	Ivgl	-	0.5	-	mA	VGL=-10V, white pattern

Note 1: HSYNC, VSYNC, DE, Digital data.

Note 2: Be sure to apply the power voltage as the power sequence spec.

Note 3: GND=AGND=0V.



■ BACKLIGHT CHARACTERISTICS

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Forward Voltage	Vf	1	21.3	ı	V	
Forward Current	If	-	160	-	mA	Ta=25±2°C,
Power Consumption	W_{BL}	-	3408	-	mW	60%RH±5%
Operating Life Time	-	30000	-	-	Hrs.	

Note: Operating life time means brightness goes down to 50% initial brightness;

The life time of LED will be reduced if LED is driven by high current, high ambient temperature and humidity conditions.



■ ELECTRO-OPTICAL CHARACTERISTICS

Paramet	er	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Notes
Response Time		Tr + Tf		-	30	40	ms	FIG 1.	4
Contrast Ratio		Cr	θ=0°	700	1000	-		FIG 2.	1
Luminance Uniformity		δ White	Ø=0° Ta=25°C	70	80	-	%	FIG 2.	3
Surface Lumin	ance	Lv		750	1000	-	cd/m ²	FIG 2.	2
			Ø = 90°	80	85	-	deg		
Viewing Angle	e	θ	Ø = 270°	80	85	-	deg	FIG 2	
Range		θ	Ø = 0°	80	85	-	deg FIG 3.	6	
			Ø = 180°	80	85	-	deg		
	D . 1	X		TBD	TBD	TBD			5
	Red	у		TBD	TBD	TBD			
	C	X		TBD	TBD	TBD			
CIE (x,y)	Green	у	$\theta=0^{\circ}$	TBD	TBD	TBD		EIC 2	
Chromaticity	DI	X	Ø=0° Ta=25°C	TBD	TBD	TBD		FIG 2.	
	Blue	у		TBD	TBD	TBD			
	XX71. 14	X		0.263	0.313	0.363			
	White	У		0.275	0.329	0.375			
NTSC	•	-	-	-	TBD	-	%	-	-

Note 1. Contrast Ratio (CR) is defined mathematically as for more information see FIG 2.

Contrast Ratio = Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)

Average Surface Luminance with all black pixels (P1,P2,P3,P4,P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)

Note 3. The uniformity in surface luminance, δ White is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

δ White = Minimum Surface Luminance with all white pixels (P1,P2,P3,P4,P5)

Maximum Surface Luminance with all black pixels (P1,P2,P3,P4,P5)

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melcher's ConoScope. Series.

Note 5. CIE (x,y) chromaticity, The x,y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.



Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope. Series Instruments for contrast ratio, Surface luminance, Luminance uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

FIG 1. The Definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "Black" and "White".

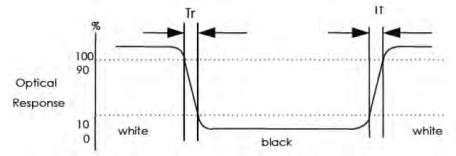
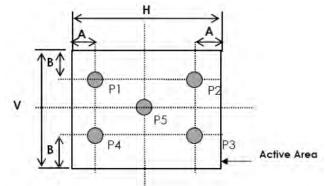


FIG 2. Measuring method for contrast ratio, surface luminance, Luminance uniformity, CIE (x,y) chromaticity.

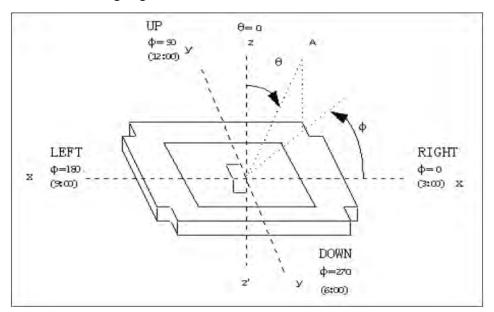


A: 5mm B: 5mm

H, V: Active Area

Light spot size \emptyset =7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-5.

FIG 3. The definition of viewing angle





■ INTERFACE DESCRIPTION

1. LCM FPC Pin Function

FPC matching connector: Hirose FH52-60S-0.5SH

AGND	Pin No.	Symbol	I/O/P	Description	Note
3	1	AGND	P	Analog ground.	
4 R0 I Data input (LSB).	2	AVDD	P	Analog power.	
S	3	VCC	P	Digital Power.	
R2	4	R0	I	· ·	
R3	5	R1	I	Data input.	
R	6	R2	I	Data input.	
9 R5 I Data input.	7	R3	I	Data input.	
10	8	R4	I	Data input.	
11	9	R5	I	Data input.	
12 G0 I Data input (LSB).	10	R6	I	Data input.	
13 G1 I Data input.	11	R7	I		
13 G1 I Data input.	12	G0	I	Data input (LSB).	
15 G3 I Data input.	13	G1	I		
16	14	G2	I	Data input.	
17 G5 I Data input.	15	G3	I	Data input.	
18 G6 I Data input.	16	G4	I	Data input.	
19 G7 I Data input (MSB). 20 B0 I Data input (LSB). 21 B1 I Data input. 22 B2 I Data input. 23 B3 I Data input. 24 B4 I Data input. 25 B5 I Data input. 26 B6 I Data input. 27 B7 I Data input (MSB). 28 DCLK I Clock input. 29 DE I Data enable signal. 30 HSD I Horizontal sync input, negative polarity. 31 VSD I Vertical sync input negative polarity. 32 MODE I H: HV mode; L: DE mode. Global reset pin, normally pull high. H: normal operation 33 RSTB I L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.	17	G5	I	Data input.	
Data input (LSB).	18	G6	I	Data input.	
Data input.	19	G7	I	Data input (MSB).	
Data input.	20	B0	I	Data input (LSB).	
Data input. Data input.	21	B1	I	Data input.	
Data input.	22	B2	I	Data input.	
Data input.	23	В3	I	Data input.	
Data input.	24	B4	I	Data input.	
Data input (MSB).	25	B5	I	Data input.	
DCLK I Clock input. DE I Data enable signal. HSD I Horizontal sync input, negative polarity. Vertical sync input negative polarity. DE/SYNC mode select, normally pull low H: HV mode; L: DE mode. Global reset pin, normally pull high. H: normal operation L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.	26	В6	I	Data input.	
DE I Data enable signal. 30 HSD I Horizontal sync input, negative polarity. 31 VSD I Vertical sync input negative polarity. DE/SYNC mode select, normally pull low H: HV mode; L: DE mode. Global reset pin, normally pull high. H: normal operation L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.	27	B7	I	Data input (MSB).	
30	28	DCLK			
31 VSD I Vertical sync input negative polarity. DE/SYNC mode select, normally pull low H: HV mode; L: DE mode. Global reset pin, normally pull high. H: normal operation L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.	29	DE	I	Data enable signal.	
DE/SYNC mode select, normally pull low H: HV mode; L: DE mode. Global reset pin, normally pull high. H: normal operation L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.	30	HSD	I	Horizontal sync input, negative polarity.	
32 MODE I H: HV mode; L: DE mode. Global reset pin, normally pull high. H: normal operation L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.	31	VSD	I		
L: DE mode. Global reset pin, normally pull high. H: normal operation L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.					
Global reset pin, normally pull high. H: normal operation L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.	32	MODE	I		
H: normal operation L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.					
33 RSTB I L: the controller is in reset state Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability. Standby mode, normally pull low.					
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circuit for stability. Standby mode, normally pull low.	33	RSTB	I		
Standby mode, normally pull low.				••	
The state of the s				·	
H: normal operation				H: normal operation	
34 STBYB I L: the controller and source driver will turn off.	3/1	STRVR	Ţ		
Suggest to connecting with an RC ($10K\Omega$, $0.47\mu F$) reset	J T	מועוט			
circuit for stability.					

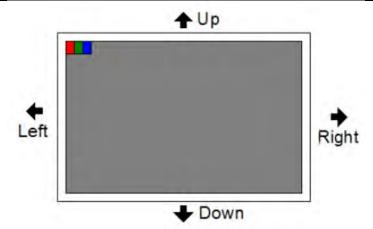


35	SHLR	I	Left or Right display control.	Note 1
36	VCC	P	Digital power.	
37	UPDN	I	Up/Down display control.	Note 1
38	GND	P	Digital ground.	
39	AGND	P	Analog ground.	
40	AVDD	P	Analog power.	
41	VCOM	I	For external VCOM DC input	
42	NC	-	No connection.	
43	BIST	I	Aging mode on/off control. Please float this pin.	
44	NC	-	No connection.	
45	NC	-	No connection.	
46	NC	-	No connection.	
47	NC	-	No connection.	
48	CSB	I	Serial communication chip selection. Please float this pin.	
49	SCL	I	Serial communication clock pin. Please float this pin.	
50	SDO	О	Serial communication data out pin. Please float this pin.	
51	SDI	I	Serial communication data pin. Please float this pin.	
52	NC	-	No connection.	
53	VPP_T	P	Power supply for trim function. Please float this pin.	
54	NC	-	No connection.	
55	NC	-	No connection.	
56	VGH	I	Positive power for TFT.	
57	VCC	P	Digital power.	
58	VGL	I	Negative power for TFT.	
59	GND	P	Digital ground.	
60	NC	-	No connection.	

I: Input; O: Output; P: Power

Note 1: SHLR and UPDN control function:

SHLR	UPDN	Data Shifting
Н	Н	Left → Right, Up → Down
L	Н	Right → Left, Up → Down
L	L	Right → Left, Down → Up
Н	L	Left → Right, Down → Up



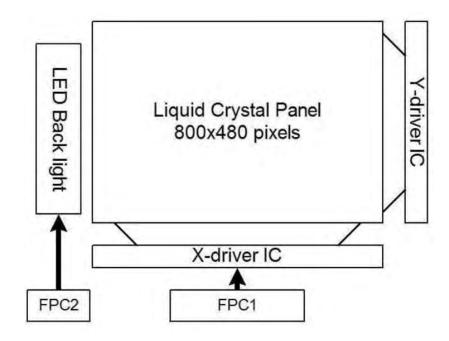


2. B/L Pin Define

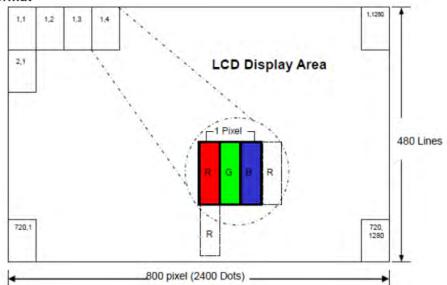
FPC matching connector: FH19SC-10S-0.5SH

Pin No.	Symbol	I/O/P	Description	Note
1	A			
2	A	P	Anode.	
3	A			
4	NC	-	No connection.	
5	K1	P	Cathode 1.	
6	K2	P	Cathode 2.	
7	K3	P	Cathode 3.	
8	K4	P	Cathode 4.	
9	NC	-	No connection.	
10	NC	-	No connection.	

■ BLOCK DIAGRAM



Pixel Format





■ APPLICATION NOTES

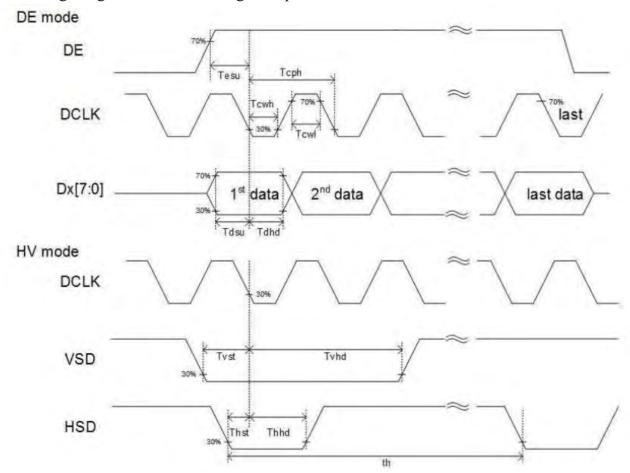
1. Input Signal Timing

1.1 Timing

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK cycle time	Tcph	20	-	220	ns	
DCLK pulse duty	Tewh	35	50	65	%	
VSD setup time	Tvst	8	-	-	ns	
VSD hold time	Tvhd	8	-	-	ns	
HSD setup time	Thst	8	-	ı	ns	
HSD hold time	Thhd	8	-	-	ns	
Data setup time	Tdsu	8	-	-	ns	
Data hold time	Tdhd	8	-	-	ns	
DE setup time	Tesu	8	-	ı	ns	
DE hold time	Tehd	8	-	ı	ns	
DCLK frequency	fclk	28	30	32	MHz	
Horizontal display area	thd		800			
HSD period time	th	899	902	915		
HSD pulse width	thpw	5	10	15	Tcph	
HSD back porch	thb		32			
HSD front porch	thfp	52	60	68		
Vertical display area	tvd		480			
VSD period time	tv	546	555	564		
VSD pulse width	tupw	6	10	14	th	
VSD back porch	tvb		5		7	
VSD front porch	tvfp	55	60	65		

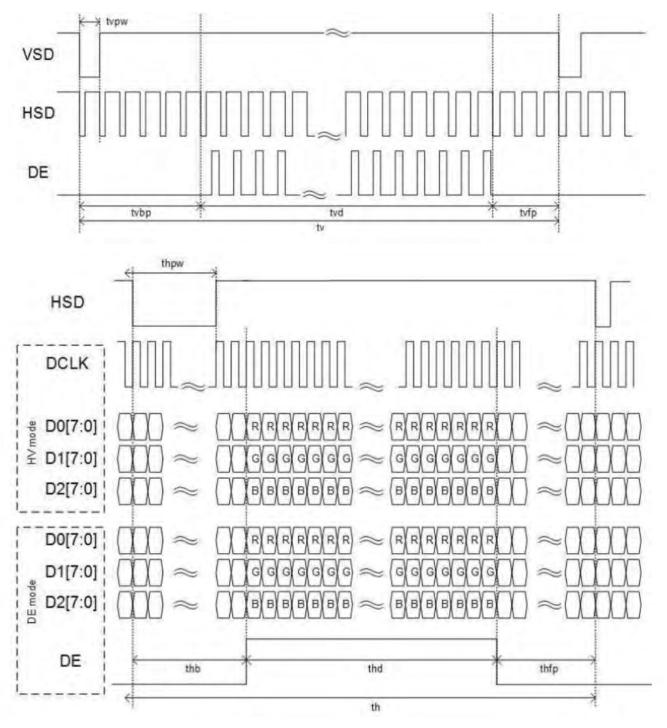


1.2 Timing Diagram of Interface Signal Input



Input clock and data timing diagram

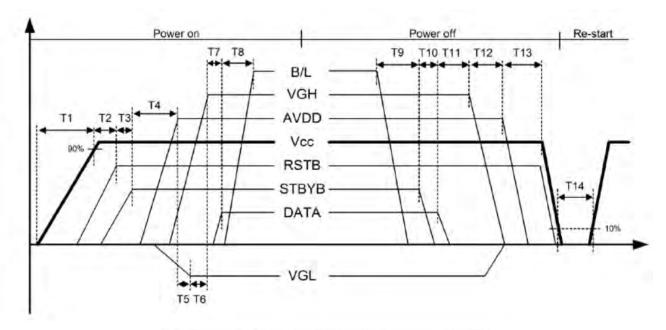




Data Input Format



1.3 Power Sequence



Item	Min.	Тур.	Max.	Unit
T1	l = 10 + 54 = 1	- 4-	20	
T2	1		- 7 P	
T3	1	19 19 1	1 Y	
T4	50	- 3-	1 to	
T5	32	13.	- :-	
T6	16	4		
T7	16	144		
T8	32		A	ms
Т9	32	.47.	- ¥-	
T10	32	4		
T11	50	100		
T12	16	0 ÷ 0	1	
T13	32	- L¥	¥	
T14	1000	2.	1 2 .	



2. Relationship Between Displayed Color and Input

		MSB LSE	MSB LSB	MSB LSB	Gray scale
	Display	R7 R6 R5 R4 R3 R2 R1 R	07 06 85 04 03 82 01 00	B7 B6 B5 B4 B3 B2 B1 B0	Level
	Black	LLLLLLLL	LLLLLLLL	LLLLLLLL	
	Blue	LLLLLLLL	LLLLLLL	ннининини	-
	Green	LLLLLLLL	нининини	LLLLLLLL	
Basic	Light Blue	LLLLLLLL	нининини	ннннннн	-
color	Red	нининини	LLLLLLLL	LLLLLLLL	
	Purple	нининини		нининини	-
	Yellow	нининин	нининини	LLLLLLLL	-
	White	нининини	нининини	нниннини	-
	Black	LLLLLLLL	LLLLLLL	LLLLLLLL	L.O
		LLLLLLL	LLLLLLLL	LLLLLLLL	L1
	Dark	LLLLLLHI	LLLLLLLL	LLLLLLLL	12
Gray scale	1	1	4	:	L3 - L251
of Red	1	HHHHHLL	LLLLLLLL	LLLLLLLL	L252
	Light	HHHHHL	LLLLLLL	LLLLLLLL	L253
		нининни	LLLLLLLL	LLLLLLLL	L254
	Red	нининини	LLLLLLL	LLLLLLL	Red L255
	Black	LLLLLLLL	LLLLLLL	LLLLLLLL	LO
		LLLLLLLL	LLLLLLLH	LLLLLLLL	L1
	Dark	LLLLLLLL	LLLLLLHL	LLLLLLL	12
Gray scale	11		1		L3L251
of Green	1	LLLLLLLL	HHHHHLL	LLLLLLLL	1.252
	Light	LLLLLLLL	HHHHHLH	LLLLLLLL	L253
		LLLLLLLL	HHHHHHL	LLLLLLLL	L254
	Green	LLLLLLLL	нининини	LLLLLLLL	Green L25
	Black	LLLLLLLL	LLLLLLLL	LLLLLLLL	LO
		LLLLLLL	LLLLLLLL	LLLLLLLH	L1
	Dark	LLLLLLLL	LLLLLLL	LLLLLLHL	L2
Gray scale	1	*****	:		L3L251
of Blue	1	LLLLLLLL	LLLLLLL	HHHHHLL	L252
	Light	LLLLLLLL	LLLLLLLL	нининги	L253
		LLLLLLLL	LLLLLLLL	HHHHHHL	1.254
	Blue	LLLLLLLL	LLLLLLLL	нининини	Blue L255
Gray scale	Black	LLLLLLLL	LLLLLLLL	LLLLLLLL	LO
		LLLLLLL	LLLLLLLH	LLLLLLLH	L1
	Dark	LLLLLLHI	LLLLLLHL	LLLLLLHL	L2
	1		+	į.	L3L251
of White & Black	4	HHHHHLL	HHHHHLL	HHHHHLL	L252
CHOCK	Light		HHHHHLH		L253
		нининни		HHHHHHL	L254
	White	нининини	ннинини	нининини	White L255



■ RELIABILITY TEST

No.	Test Item	Test Condition	Remark
1	High Temperature Storage Test	$95^{\circ}\text{C} \pm 2^{\circ}\text{C} / 1000\text{Hrs}.$	Note2
2	Low Temperature Storage Test	$-40^{\circ}\text{C} \pm 2^{\circ}\text{C} / 1000\text{Hrs}.$	Note 1, 2
3	High Temperature Operating Test	$85^{\circ}\text{C} \pm 2^{\circ}\text{C} / 1000\text{Hrs}.$	
4	Low Temperature Operating Test	$-30^{\circ}\text{C} \pm 2^{\circ}\text{C} / 1000\text{Hrs}.$	Note 1
5	High Temperature and High Humidity Operation Test	60 ± 5°C, 90%RH 1000Hrs.	Note 1, 2
6	Thermal Shock Test	-40±2°C(30Min.)~25±2°C(5Min.)~85±2°C(30Min.) 1000Hrs	
7	Vibration Test	1. Random: 1.04G, 5~500Hz, X,Y,Z, 30min/each direction 2. Sine: Freq. Range: 8~33.3Hz, Stoke: 1.3mm Sweep: 2.9G, 33.3~400Hz X/Z: 2hrs, Y:4hrs	
8	Shock Test	Half-Sine, 100G, 6ms, ±X,Y,Z, 1time	
9	Drop Test (With Carton)	Drop height condition, basis on the product weight and Follow QB100-0027 1 corner, 3 edges, 6 surfaces	
10	Vibration (With Carton)	Random: 0.015G^2/Hz, 5~200Hz -6dB/Octave, 200~400Hz X,Y,Z each direction 2hrs	
11	Electro Static Discharge Test	±200V,200pF (0_) 1 time/each termina	

Note 1: Without water condensation.

Note 2: The function test shall be conducted after 2 hours storage at the room temperature and humidity after removed from the test chamber.



■ INSPECTION CRITERION

OUTGOING QUALITY STANDARD	PAGE 1 OF 4
TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA	

This specification is made to be used as the standard acceptance/rejection criteria for TFT module.

1. Sample Plan

1.1 Lot size: Quantity per shipment lot per model

1.2 Sampling type: Normal inspection, Single sampling

1.3 Inspection level: II

1.4 Sampling table: MIL-STD-105D1.5 Acceptable quality level (AQL)

Major defect: AQL=0.65 Minor defect: AQL=1.50

2. Inspection Condition

2.1 Ambient conditions

a. Temperature: Room temperature 25±5°C

b. Humidity: (60±10) %RH

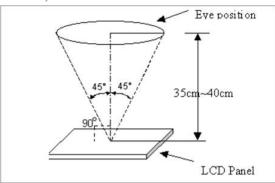
c. Illumination: Single fluorescent lamp non-directive (300 to 700 Lux)

2.2 Viewing distance

The distance between the LCD and the inspector's eyes shall be at least 35±5cm.

2.3 Viewing angle

U/D: 45° / 45°, L/R: 45° / 45°



3. Definition of Inspection Item

3.1 Definition of inspection zone in LCD module (LCM)

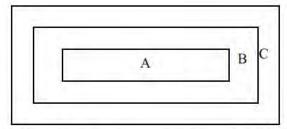


Fig. 1 Inspection zones in an LCD

Zone A: Character / Digit area (Active area)

Zone B: Viewing area except Zone A (Zone A + Zone B=minimum viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)



OUTGOING QUALITY STANDARD	PAGE 2 OF 4
TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA	

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product. If any visual defect in Zone C is impermissible, customers need to inform us by written.

4. Inspection Plan

Defects are classified as major defects and minor defects according to the degree of defectiveness defined herein.

4.1 Major defect

Item No.	Items To Be Inspected	Inspection Standard
4.1.1	All Functional Defects	 No display Display abnormally Short circuit Line defect Excess power consumption
4.1.2	Missing	Missing function component
4.1.3	Crack	Glass crack

4.2 Minor defect

Item No.	Items To Be Inspected	Inspection Standard	
4.2.1	Spot Defect Including Black spot White spot Pinhole Foreign particle Polarizer dirt	For dark / white spot is defined $\varphi = (\mathbf{x} + \mathbf{y}) / 2$ $\Rightarrow \mathbf{x} \Rightarrow \mathbf{y}$ Size $\varphi(\text{mm})$ $\varphi \le 0.25$ $2\text{mm}(\text{min}) \text{ apart}$ $0.25 < \varphi \le 0.50$ $5\text{mm}(\text{min}) \text{ apart}$ $0.50 < \varphi$	Acceptable Quantity Ignore 5 Not allowed



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		Defined	
		Length Width	
4.2.2	Line Defect Including Black line	Width (mm) Length (mm)	Acceptable Quantity
	White line	$W \le 0.05$ and $L \le 10$	Ignore
	Scratch	$0.05 < W \le 0.08$ and $L \le 10$ 5mm(min) apart	5
		$0.08 < W \le 0.10$ and $L \le 5$ 5mm(min) apart	3
		0.10 < W or 10 <l< td=""><td>Not allowed</td></l<>	Not allowed
		Size φ(mm)	Acceptable Quantity
	Polarizer Dent / Bubble	φ ≤ 0.30	Ignore
4.2.3		Non visible area	Ignore
		$0.30 < \varphi \le 0.50$ 5mm(min) apart	5
		0.50 < φ	Not allowed
4.2.4	Electrical Dot Defect	_ = =	Full black, Red, Green and Blue
7.2.7	Licenteal Dot Detect	screens	Acceptable Quantity
		Black dot defect	Acceptable Quantity 5
		Bright dot defect	2
		Total Dot	5

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OUTGOING QUALITY STANDARD	PAGE 4 OF 4
TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA	

		1. Corner chips:	
		Size (mm)	Acceptable Quantity
		$X \le 3mm$ $Y \le 3mm$ $Z \le T$	Ignore T: Glass thickness X: Length Y: Width Z: Thickness
4.2.5	Touch Panel Chips	2. Side chips:	
		Size (mm)	Acceptable Quantity
		$X \le 5mm$ $Y \le 3mm$ $Z \le T$	Ignore T: Glass thickness X: Length Y: Width Z: Thickness
4.2.6	Touch Panel Newton Ring	Compare with	n limit sample

Note: 1. Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area.

- 2. The distance between black dot defects or black and bright dot defects should be more than 5mm apart. The distance between two bright dot defects should be more than 15mm apart.
- 3. Polarizer bubble is defined as the bubble appears on active display area. The defect of polarizer bubble shall be ignored if the polarizer bubble appears on the outside of active display area.
- 4. Mura is checker by 6% ND filter.
- 5. Foreign particle on the surface of the LCM should be ignore.



■ PRECAUTIONS FOR USING LCD MODULES

♦ Handing Precautions

- 1. The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Dot not subject it to a mechanical shock by dropping it or impact.
- 2. If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- 3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- 4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- 5. If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents.
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- 6. Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents
- 7. Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.
- 8. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- 9. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- 10. Do not attempt to disassemble or process the LCD module.
- 11. If the logic circuit power is off, do not apply the input signals.
- 12. Electro-Static Discharge Control. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- 13. Electro-Static Discharge Control. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.



- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist LCM.

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♦ Handing Precaution for LCM

LCM is easy to be damaged.

Please note below and be careful for handling!

Correct handling:





As above picture, please handle with anti-static gloves around LCM edges.

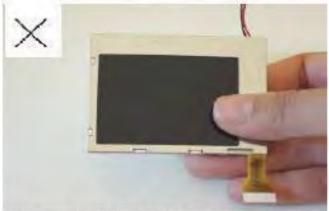
Incorrect handling:



Please don't touch IC directly.



Please don't stack LCM.



Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.



♦ Handing Precaution for LCD

LCD is easy to be damaged.

Please note below and be careful for handling!

Correct handling:





As above photo, please handle with anti-static gloves around LCD edges.

Incorrect handling:



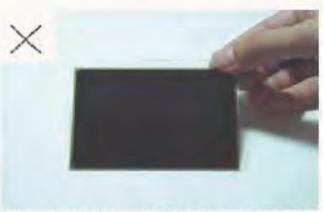
Please don't stack the LCDS.



Please don't hold the surface of LCD.



Please don't operate with sharp stick such as pens.



Please don't touch ITO glass without anti-static gloves.



♦ Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- 1. Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- 2. Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- 3. The polarizer surface should not come in contact with any other objects. (We advise you to store them in the anti-static electricity container in which they were shipped.

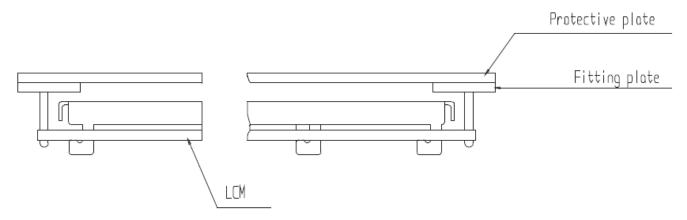
♦ Others

- 1. Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- 2. If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 3. To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
 - Exposed area of the printed circuit board.
 - Terminal electrode sections.

Using LCD Modules

1. Installing LCD Modules

- 1.1 The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.
- 1.1.1 Cover the surface with a transparent protective plate to protect the polarizer and LC cell.

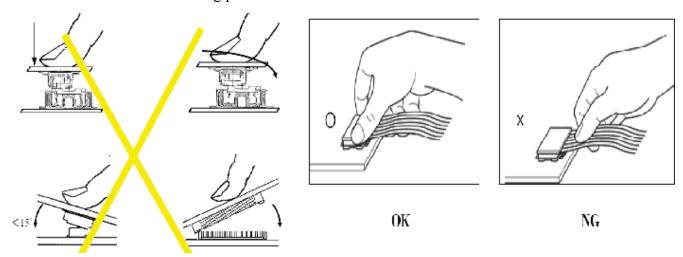


1.1.2 When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.



2. Precaution For Assemble The Module With BTB Connector

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows.



Precaution For Soldering To The LCM

	Hand Soldering	Machine Drag Soldering	Machine Press Soldering
No ROHS Product	290°C ~ 350°C. Time: 3~5S.	330°C ± 350°C. Speed: 4~8mm/s.	300°C ± 330°C. Time: 3~6S. Press: 0.8~1.2Mpa
ROHS Product	340°C ~ 370°C. Time: 3~5S.	350°C ± 370°C. Speed: 4~8mm/s.	330°C ± 360°C. Time: 3~6S. Press: 0.8~1.2Mpa

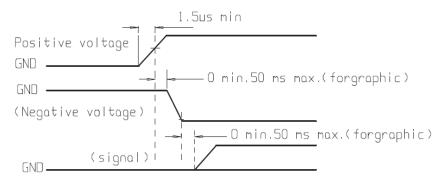
- 1. If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- 2. When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- 3. When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

Precaution For Operation

- 1. Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- 2. It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- 3. Response time will be extremely delayed at lower temperature than the operating temperature range and on the mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature.
- 4. If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- 5. A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
- 6. Input each signal after the positive/negative voltage becomes stable.



7. Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.



♦ Safety

- 1. It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- 2. If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

♦ Limited Warranty

Unless agreed between All Shore and customer, All Shore will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with All Shore LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to All Shore within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability is limited to repair and/or replacement on the terms set forth above. All Shore will not be responsible for any subsequent or consequential events.

♦ Return LCM Under Warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- Broken LCD glass.
- PCB eyelet is damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

■ PRIOR CONSULT MATTER

- 1. For All Shore standard products, we keep the right to change material, process ... for improving the product property without notice on our customer.
- 2. For OEM products, if any change needed which may affect the product property, we will consult with our customer in advance.
- 3. If you have special requirement about reliability condition, please let us know before you start the test on our samples.