



ASI-T-500MA10FN/W

No	Item	Specification	Remark
1	Type	Transmissive	--
2	Display Mode	Normally Black	--
3	Pixel Element	a-Si TFT	--
4	Screen Size	5.0 inch	--
5	Resolution	800(RGB) x 480	--
6	Color Number	16.7M	
7	Active Area	108.0 (W) x 64.8(L) (mm)	--
8	Pixel Size	0.135 x 0.135 (mm)	--
9	Color Arrangement	RGB-stripe	--
10	Assembly Type	COG	--
11	Back Light	LED	--
12	Viewing Direction	Free	--
13	Weight	TBD	g
14	Module Dimension	120.7(W) x 75.8(L) x 2.9(H) (mm)	



RECORD OF REVISION

DATE	REV.	PAGE	SUMMARY

3. General specifications

3.1 General specifications

It is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses the amorphous silicon TFT as a switching devices. This model is composed of a Transmissive type TFT-LCD Panel, a driver circuit and a back-light unit.

3.2 Features

- High image quality a-Si TFT LCD module.
- 16.7M color number.
- Support 24-bit parallel (RGB) input mode
- High contrast, high brightness
- Low power consumption.

4. Mechanical data

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5. Absolute maximum ratings

5.1 Electrical absolute maximum ratings

(1) TFT-LCD Panel Absolute Maximum Ratings

Ta=25°C

Item	Symbol	Condition	Standard Value		Unit	Remark
			Min.	Max.		
Power Supply Voltage	VDD	GND=0V	-0.3	4.0	V	--

* If the LSI is used above these absolute maximum ratings, it may become permanently damaged. Using the LSI within the following electrical characteristics limit is strongly recommended for normal operation. If these electrical characteristic conditions are also exceeded, the LSI will malfunction and cause poor reliability.

(2) Back-Light Unit

Ta=25°C

Item	Symbol	Min.	Max.	Unit	Remark
Current	I _{LED}	--	180	mA	--

5.2 Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit	Remark
Operation temperature range	Top	-20	70	°C	Ambient
Storage temperature range	Tst	-30	80	°C	Ambient

(1) Corrosive gas environment is not acceptable.

(2) TFT-LCD color will change slightly depending on environment temperature.
This phenomenon is reversible.

6. Electrical characteristics

6.1 TFT-LCD Module

Ta=25°C

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Power Supply Voltage	VDD	3.1	3.3	3.6	V	--
Operating Current	IDD	--	TBD	--	mA	--
Hight Level Input Voltage	VIH	0.7VDD	--	VDD	V	NOTE (1)
Low Level input Voltage	VIL	GND	--	0.3VDD	V	

NOTE(1) : CLK , DE , R0~R7 , G0~G7 , B0~B7

6.2 Back-Light Unit

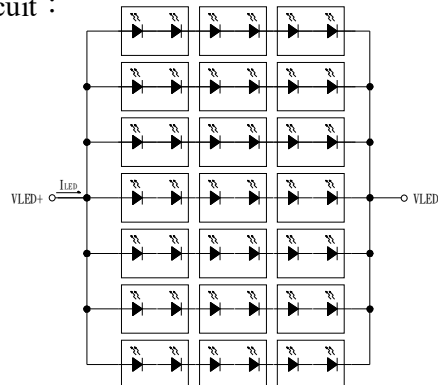
Ta=25°C

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Forward Voltage	V _{LED}	18.0	19.2	20.4	V	NOTE (1)
Forward Current	I _{LED}	--	120	--	mA	--

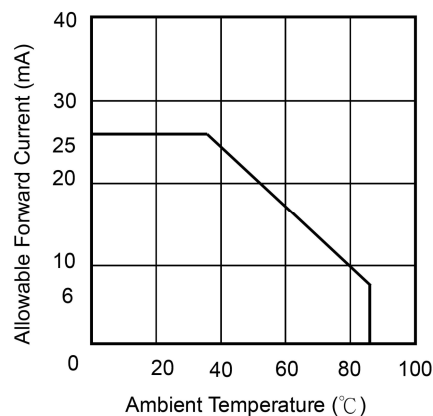
NOTE (1) : The LEDs is serial type.

NOTE (2) : The “LED life time” is defined as the module brightness decreases to 50% of original brightness that the ambient temperature is 25°C and I_{LED}=120mA .
The LED lifetime could be decreased if operating I_{LED} is lager than 120mA.

NOTE (3) : Back-light circuit :



NOTE (4) : Current reduction rate of LED backlight is according to the graph indicated below :

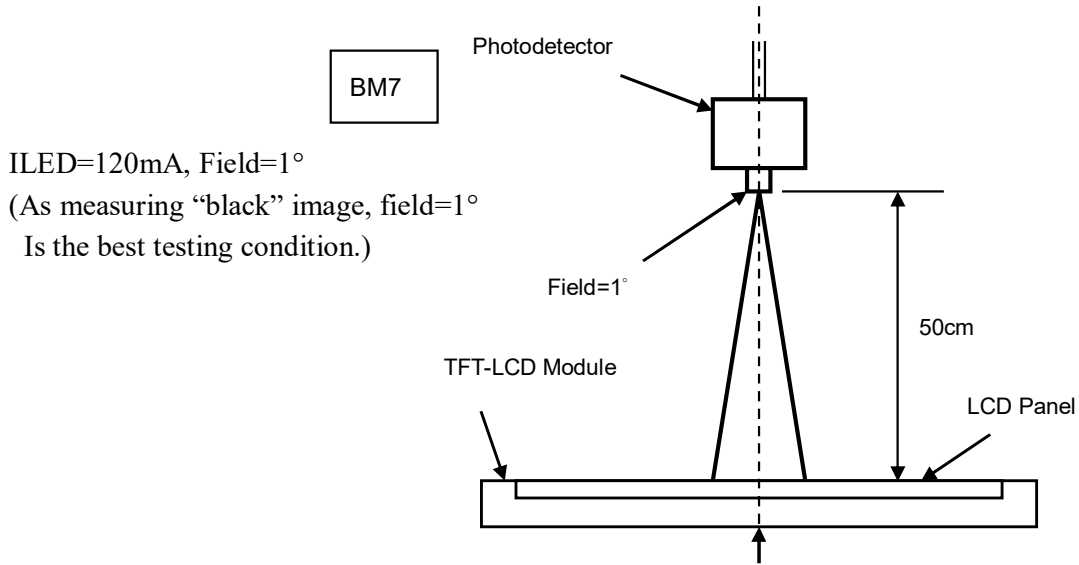


7. Optical characteristics

$T_a=25^{\circ}C$, $I_{LED}=120mA$

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Brightness		B	$\theta=0^{\circ}$ Normal viewing angle At the center of panel	900	1000	--	cd/m ²	(1)
Contrast Ratio		C/R		800	1000	--	--	(2)
Response Time		Tr + Tf		--	30	40	ms	(3)
Color Gamut		(%)		45	50		%	
Color chromaticity	White	Wx		(0.270)	(0.320)	(0.370)	--	--
		Wy		(0.295)	(0.345)	(0.395)	--	--
	Red	Rx		(0.579)	(0.629)	(0.679)	--	--
		Ry		(0.276)	(0.326)	(0.376)	--	--
	Green	Gx		(0.287)	(0.337)	(0.387)	--	--
		Gy		(0.496)	(0.546)	(0.596)	--	--
	Blue	Bx	(0.086)	(0.136)	(0.186)	--	--	
		By	(0.093)	(0.143)	(0.193)	--	--	
Viewing Angle	Top	θ_U	C/R \geq 10 Backlight On	70	80	--	deg.	(4)
	Bottom	θ_D		70	80	--		
	Left	θ_L		70	80	--		
	Right	θ_R		70	80	--		
Uniformity		Un	$\theta=0^{\circ}$ Normal viewing angle	70	--	--	%	(5)

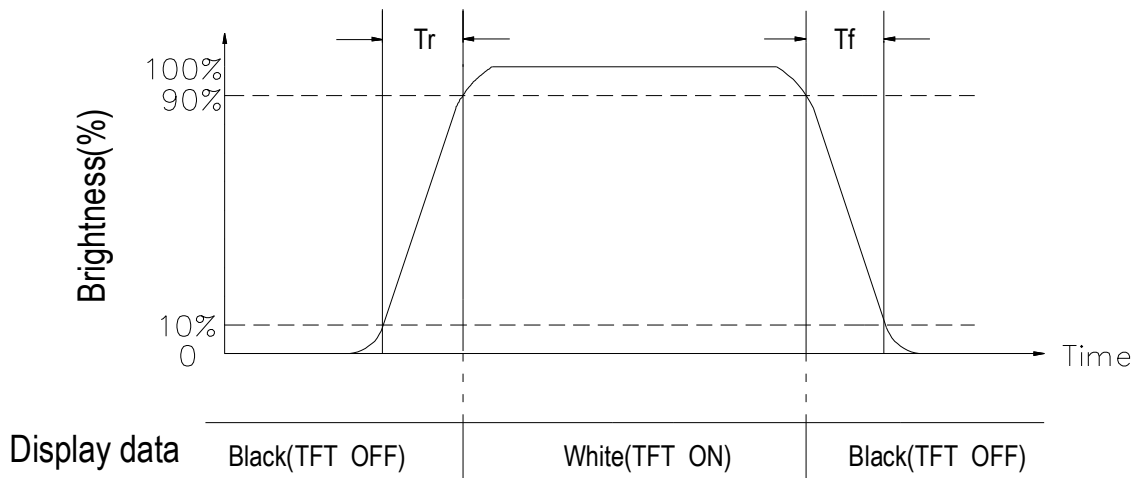
NOTE 1 : The brightness test equipment setup



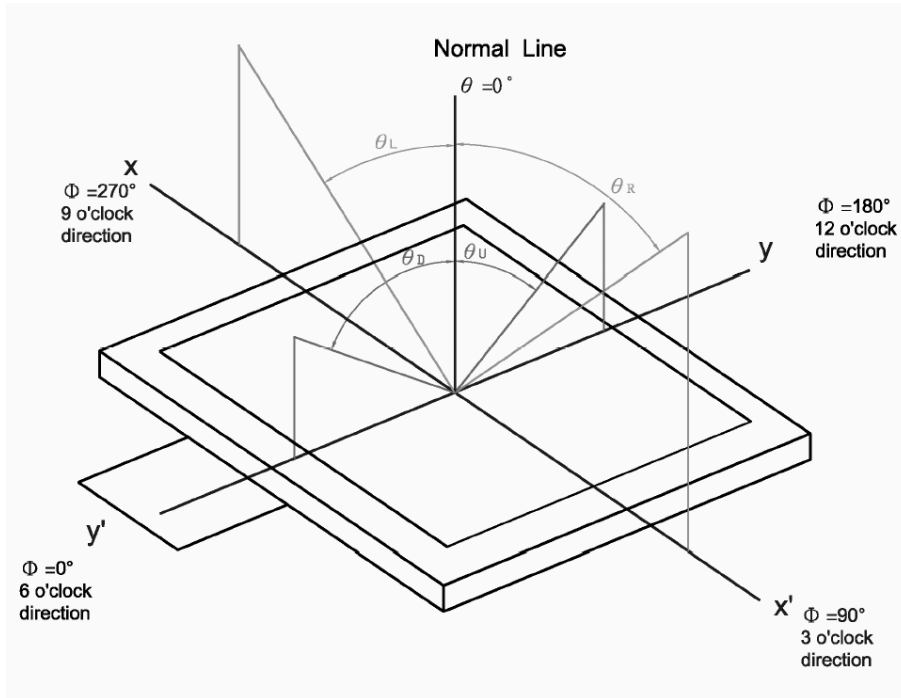
NOTE 2 : Definition of contrast Ratio (C/R)

$$C/R = \frac{\text{Brightness When LCD is at "White" State}}{\text{Brightness When LCD is at "Black" State}}$$

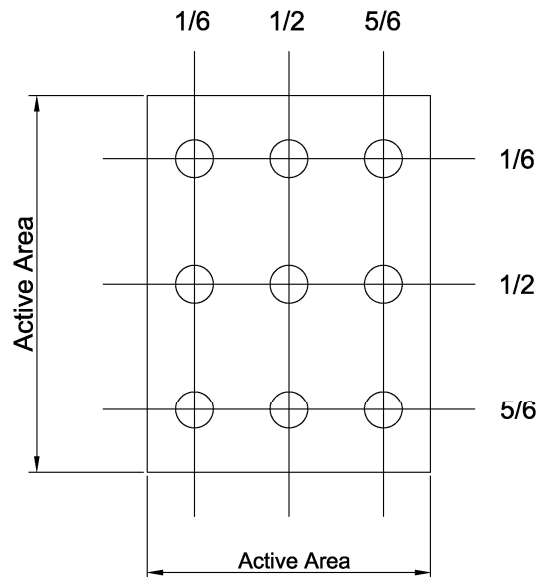
NOTE 3 : Definition of response time



NOTE 4 : Definition of viewing angle

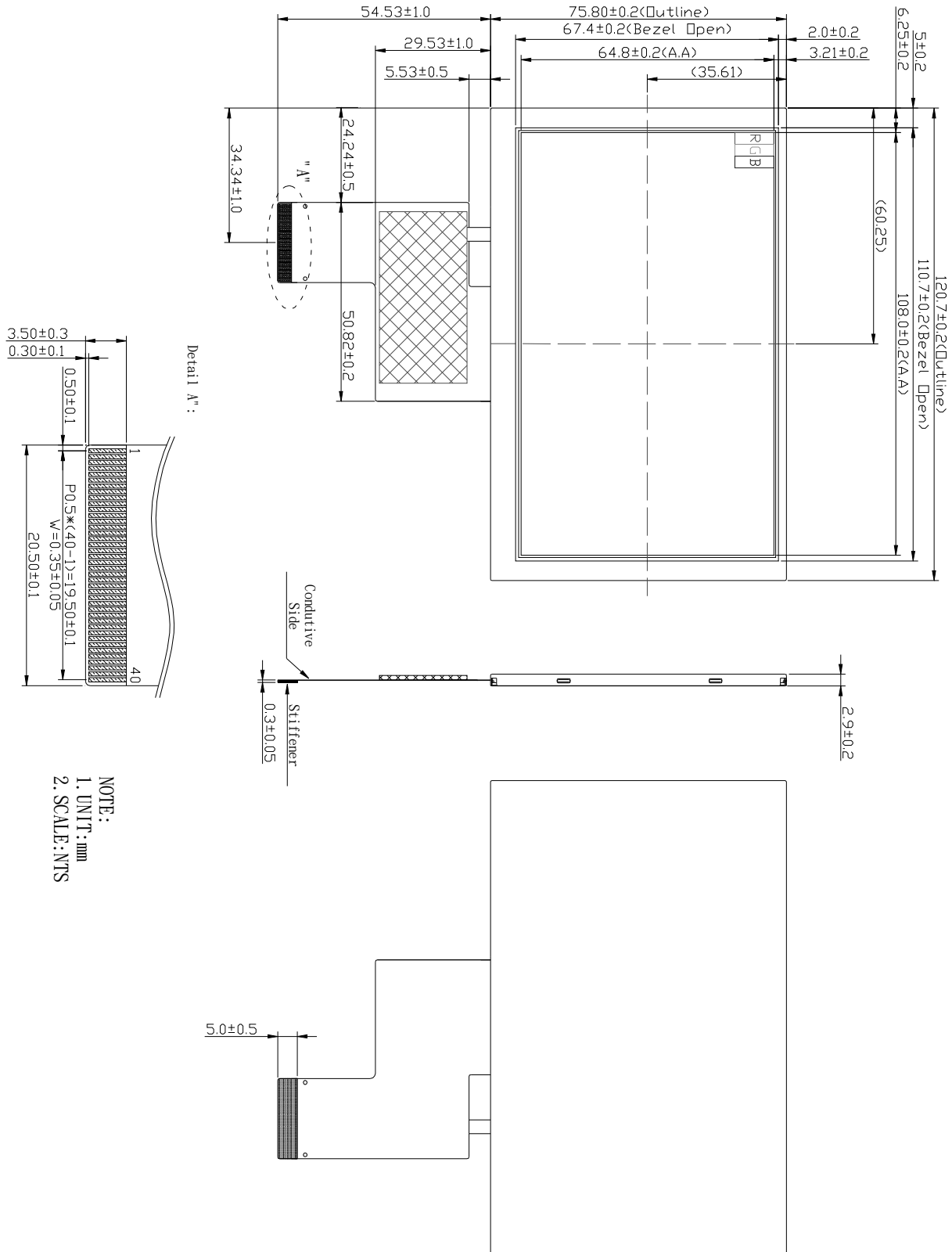


NOTE 5 : Definition of uniformity (Un)



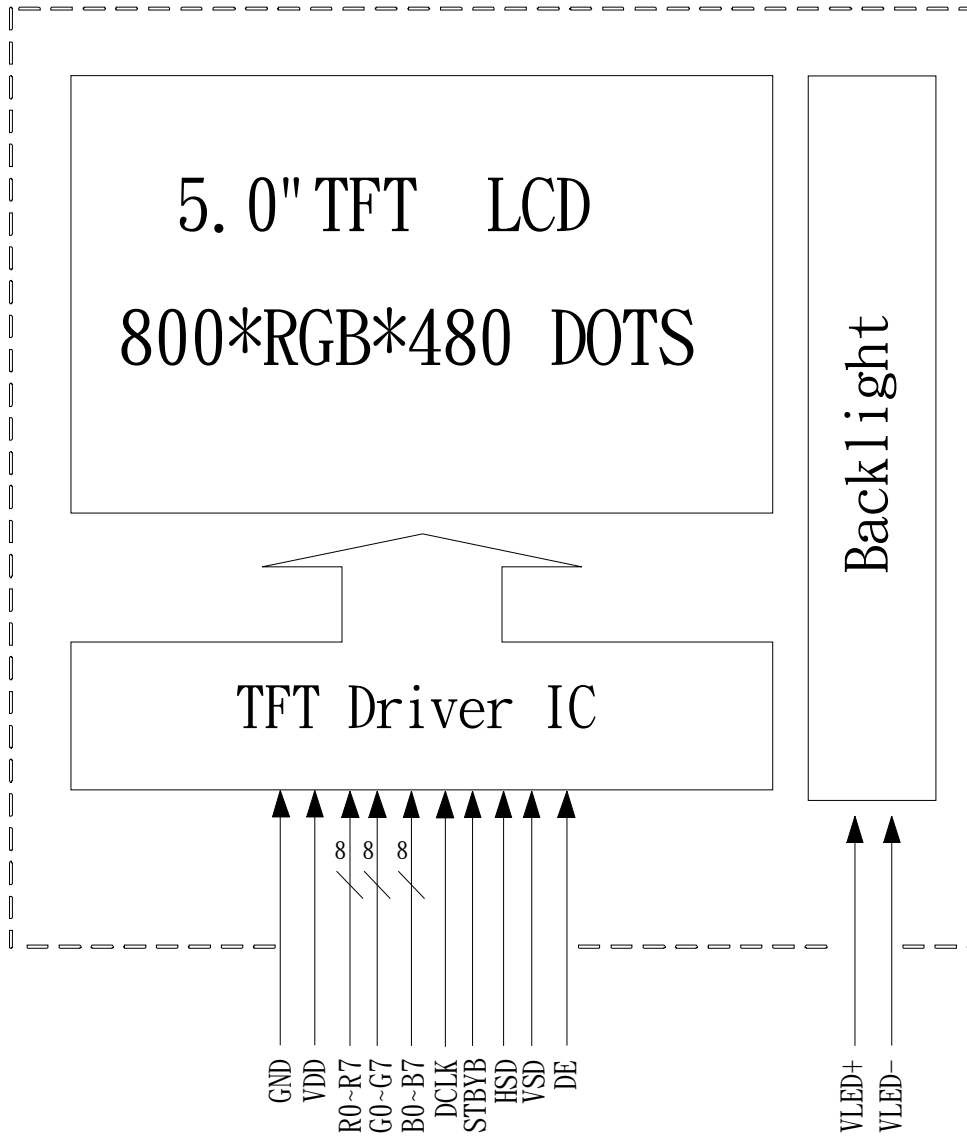
$$U_n = \frac{B_{min}}{B_{max}} \times 100\%$$

8. Outline dimension



9. Block diagram

9.1 TFT-LCD Module (Interface System Structure)



10. Input Terminal Pin Assignment

10.1 Input Signal & Power

Pin no	Symbol	Description	Remark
1	VLED-	LED Cathode	-
2	VLED+	LED Anode	-
3	GND	Ground pins	-
4	VDD	Power supply for digital circuits	-
5	R0	Red data bit 0	-
6	R1	Red data bit 1	-
7	R2	Red data bit 2	-
8	R3	Red data bit 3	-
9	R4	Red data bit 4	-
10	R5	Red data bit 5	-
11	R6	Red data bit 6	-
12	R7	Red data bit 7	-
13	G0	Green data bit 0	-
14	G1	Green data bit 1	-
15	G2	Green data bit 2	-
16	G3	Green data bit 3	-
17	G4	Green data bit 4	-
18	G5	Green data bit 5	-
19	G6	Green data bit 6	-
20	G7	Green data bit 7	-
21	B0	Blue data bit 0	-
22	B1	Blue data bit 1	-
23	B2	Blue data bit 2	-
24	B3	Blue data bit 3	-
25	B4	Blue data bit 4	-
26	B5	Blue data bit 5	-
27	B6	Blue data bit 6	-
28	B7	Blue data bit 7	-
29	GND	Ground pins	-
30	DCLK	Clock signal for data latching and internal counter of	-
31	STBYB	Standby mode control. STBYB="L", enter standby mode for power saving. Timing controller and source driver will turn off, all outputs are Hi-Z.	-
32	HSD	Horizontal sync input	-
33	VSD	Vertical sync input	-
34	DE	Data Enable Control	-
35	NC	Not connection	-
36	GND	Ground pins	-



Pin no	Symbol	Description	Remark
37	NC(XR)	Not connection, Reserve for RTP	-
38	NC(YD)	Not connection, Reserve for RTP	-
39	NC(XL)	Not connection, Reserve for RTP	-
40	NC(YU)	Not connection, Reserve for RTP	-

11. Timing Characteristics

11.1 AC Characteristics

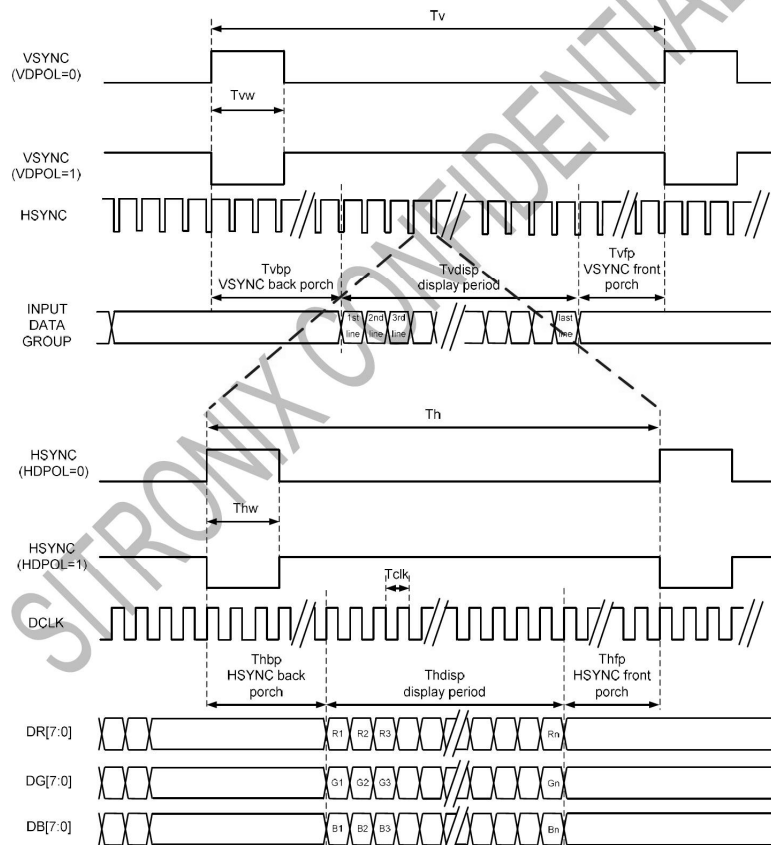
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
VDD Power Source Slew Time	TPOR	-	-	20	ms	From 0V to 99% VDD
GRB Pulse Width	tRSTW	10	50	-	us	R=10Kohm, C=1uF
SD Output Stable Time	Tst	-	-	12	us	Output settled within +20mV Loading = 6.8k+28.2pF.
GD Output Rise and Fall Time	Tgst	-	-	6	us	Output settled (5%~95%), Loading = 4.7k+29.8pF

11.2 RGB Interface

RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

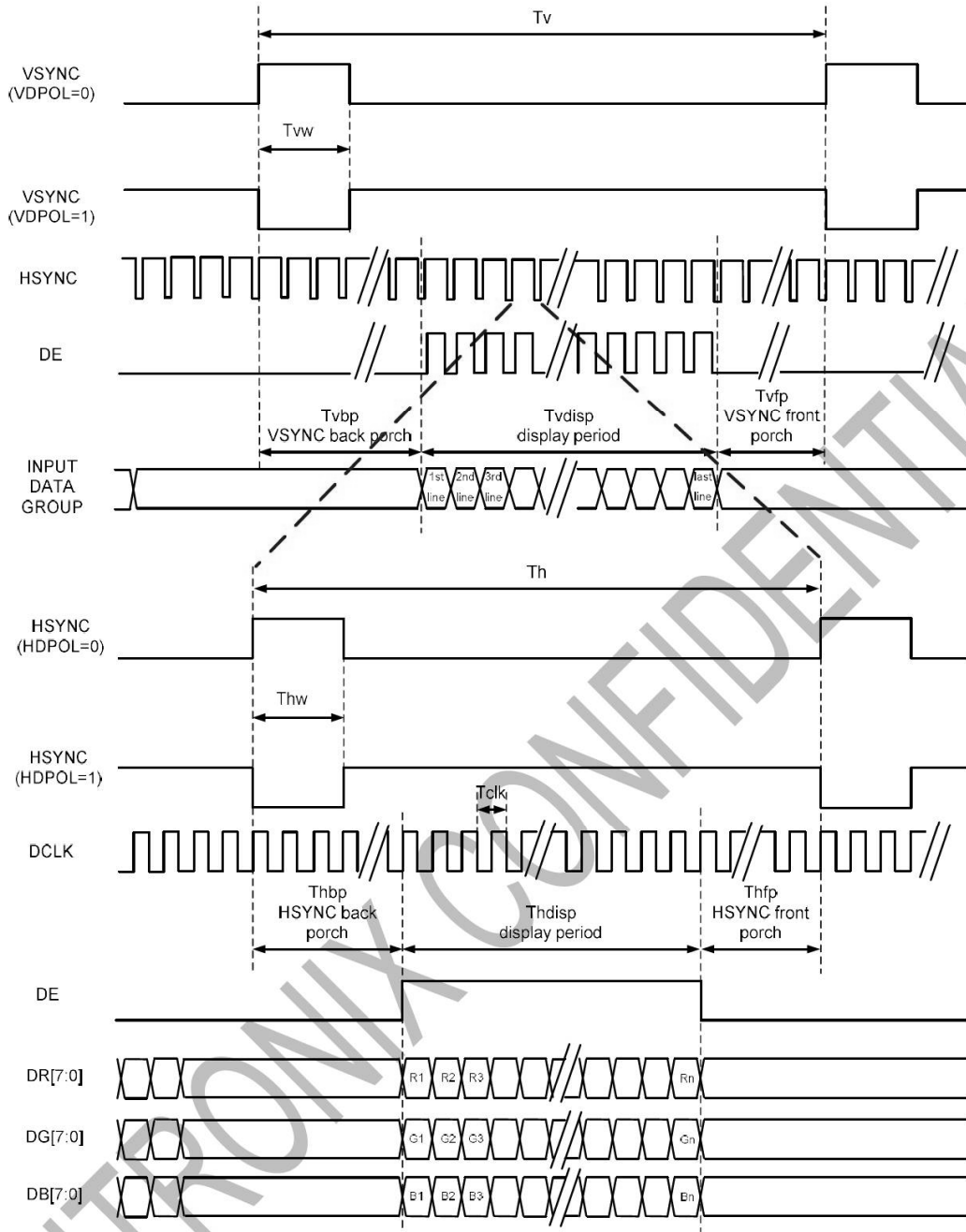
Note: "Input" means these signals are driven by host side

11.2.1 SYNC Mode



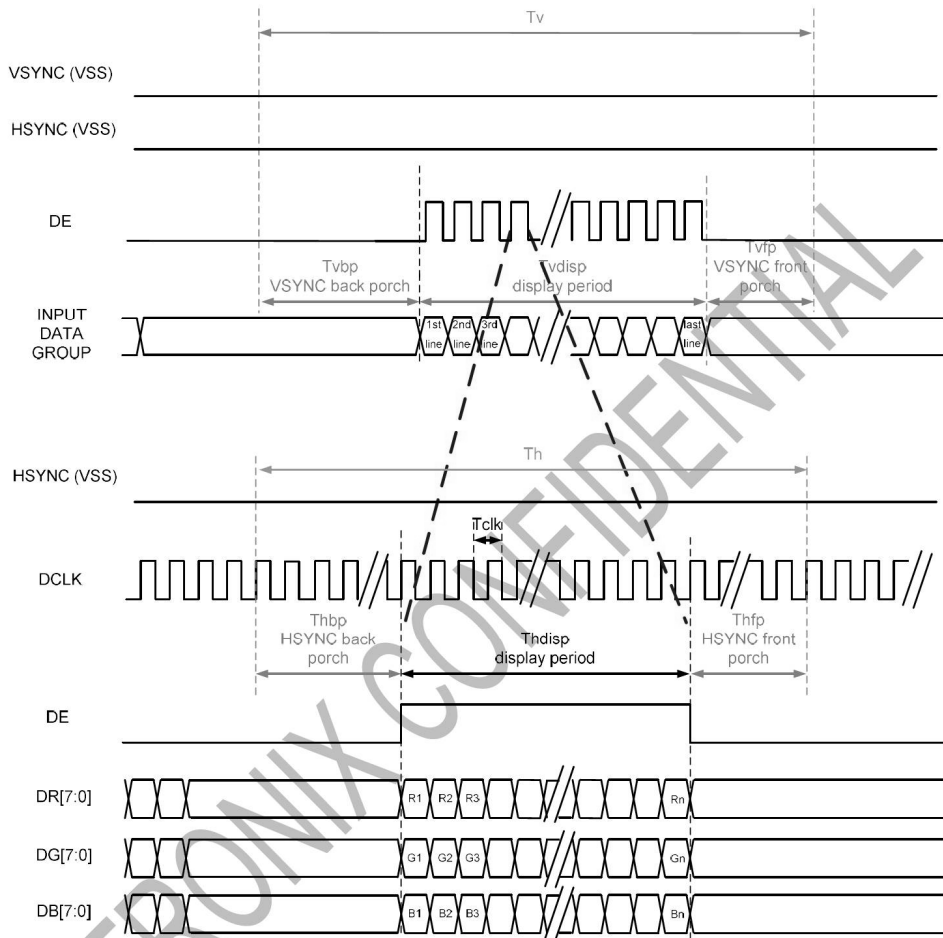
Note : LCM VDPOL=1, HDPOL=1.

11.2.2 SYNC-DE Mode



Note : LCM VDPOL=1, HDPOL=1.

11.2.3 DE Mode



11.2.4 Parallel 24-bit RGB Input Timing Table ¹

Parallel 24-bit RGB Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C).

Parallel 24-bit RGB Interface Timing Table							
Item	Symbol	Min.	Typ.	Max.	Unit	Remark	
DCLK Frequency	Fclk	23	25	27	MHz	.	
HSYNC	Period Time	T_h	808	816	848	DCLK	
	Display Period	T_{hdisp}	800			DCLK	
	Back Porch	T_{hbp}	4	8	24	DCLK	
	Front Porch	T_{hfp}	4	8	24	DCLK	
	Pulse Width	T_{hw}	2	4	8	DCLK	
VSYNC	Period Time	T_v	496	512	528	HSYNC	
	Display Period	T_{vdisp}	480			HSYNC	
	Back Porch	T_{vbp}	8	16	24	HSYNC	
	Front Porch	T_{vfp}	8	16	24	HSYNC	
	Pulse Width	T_{vw}	2	4	8	HSYNC	

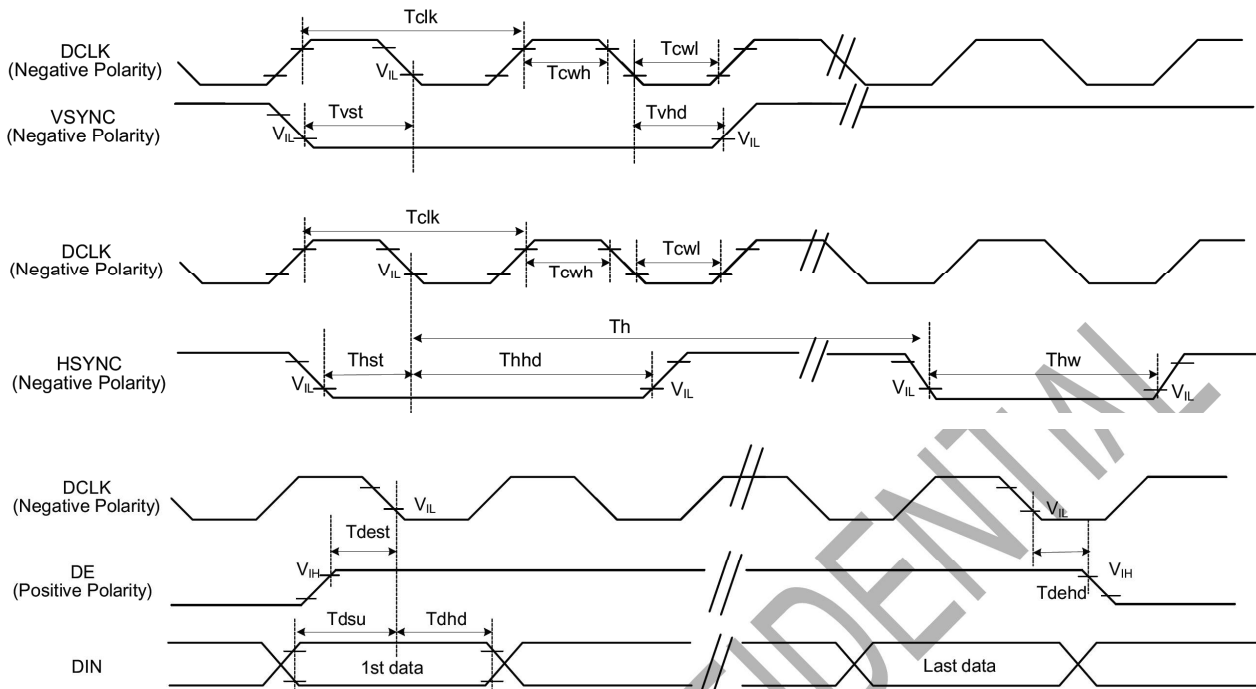
Note: 1. The minimum blanking time depends on the GIP timing of the panel specification.

2. To ensure the compatibility of different panels, it is recommended to use the typical setting.

3. It is necessary to keep $T_{vbp} = 16$ and $T_{hbp} = 8$ in sync mode. DE mode is unnecessary to keep it.

4. The maximum DCLK Frequency is 27MHz. If the case needs faster DCLK, please contact Sitronix.

11.3 System Bus Timing for RGB Interface



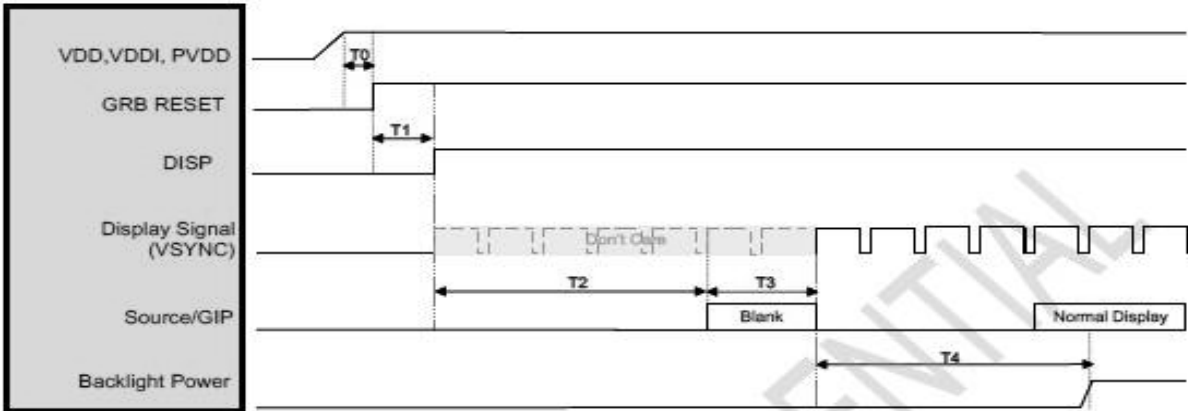
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK Pulse Duty	T_{cw}	40	50	60	%	
VSYNC Setup Time	T_{vst}	10	-	-	ns	
VSYNC Hold Time	T_{vhd}	10	-	-	ns	
HSYNC Setup Time	T_{hst}	10	-	-	ns	
HSYNC Hold Time	T_{hhd}	10	-	-	ns	
Data Setup Time	T_{dsu}	10	-	-	ns	
Data Hold Time	T_{dhd}	10	-	-	ns	
DE Setup Time	T_{dest}	10	-	-	ns	
DE Hold Time	T_{dehd}	10	-	-	ns	



ALL SHORE INDUSTRIES

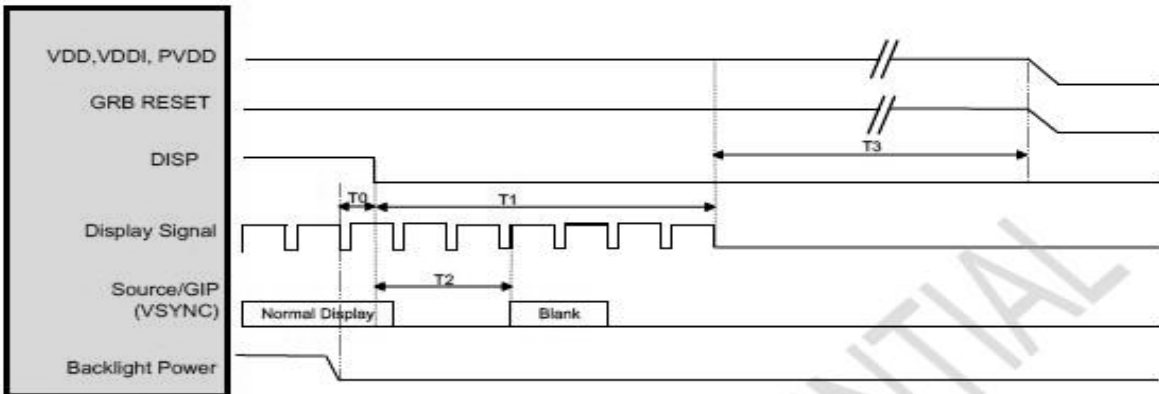
11.4 Power On/Off Sequence

Power On Sequence



Symbol	Description	Time	Unit
T0	System power stability to GRB RESET signal	≥1	ms
T1	GRB RESET= "High" to DISP="High"	≥10	ms
T2	DISP="High" to Source/GIP scan blank	85	ms
T3	IC scan blanking signal	≥33	ms
T4	Display signal input to Backlight power on (base on Display Signal Frame Rate 60Hz)	≥100	ms

Power Off Sequence



Symbol	Description	Time	Unit
T0	Backlight Power off to DISP="Low"	≥1	ms
T1	DISP="Low" to IC internal voltage discharge complete	≥100	ms
T2	DISP="Low" to Source/GIP scan blank (base on Display Signal Frame Rate 60Hz)	≤50	ms
T3	IC internal voltage discharge is completed to VDD/VDDI/PVDD off	≥0	ms

Note: 1. When DISP pull "H" or "L", IC will execute the internal power on or power off procedures. Please be careful about the timing of DISP and do not interrupt it during power on or power off procedure, otherwise unexpected errors will occur.

2. RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0].

3. LVDS interface Display signal: DCLK P/N; RX[3:0] P/N.

12. Driver IC Control Algorithms

1. Refer to the data Sheet of LCD DRIVER IC1 ST7265 or equivalent.

13. Reliability Test Items

No.	Test items	Conditions	Remark
1	High temperature operation	70°C , 240hours	--
2	Low temperature operation	-20°C , 240hours	--
3	High temperature storage	80°C , 240hours	
4	Low temperature storage	-30°C , 240hours	
5	High temperature & high humidity storage	40°C , 90% RH , 120hours	--
6	Thermal Shock storage	-20°C , 30min. ~ 70°C , 30min. , 100 Cycles	--
7	Vibration test	Freq.:10~55~10~55~10 Hz, Amplitude : 1.5 mm. 2 hours for each direction of X, Y, Z	Non-operation
8	Electrostatic discharge	±2KV, Human Body Mode, 100pF / 1500Ω	Non-operation
<p>Criterion: There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.</p>			

14. General Precautions

Please pay attentions to the followings as using the LCD module.

14.1 Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the polarizer is very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.
- (e) Avoid using Ketone type materials (e.g. Acetone), Toluene, Ethyl acid or Methyl chloride to clean the display surface. It might damage the polarizer permanently. The recommended solvents are water and Isopropyl alcohol.
- (f) Wipe off water droplets or oil immediately.
- (g) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (h) Do not touch the output pins directly with bare hands.
- (i) Do not disassemble the LCD module.

14.2 Storage

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

14.3 Operation

- (a) When mounting or dismounting the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms should always be obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.
- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.

14.4 Others

- (a) If the liquid crystal leaks from the panel, it should be kept away from the eyes or mouth.
- (b) For the fragility of polarizer, it is recommended to attach a transparent protective plate over the display surface.
- (c) It is recommended to peel off the protection film on the polarizer slowly so that the electrostatic charge can be minimized.

15. Quality and reliability

15.1 Test condition

Test should be conducted under the following conditions:

- (a) Ambient temperature: $25 \pm 5^{\circ}\text{C}$
- (b) Humidity: $55 \pm 10\% \text{ RH}$

15.2 Sampling plan

Sampling method shall be in accordance with MIL-STD-105D, inspection level II, normal inspection, and single sampling plan tables for normal tightened and reduced inspection.

15.3 Acceptable quality level

A major defect is a defect that could result in failure or materially reduce that the usability of the unit of product for its intended purpose.

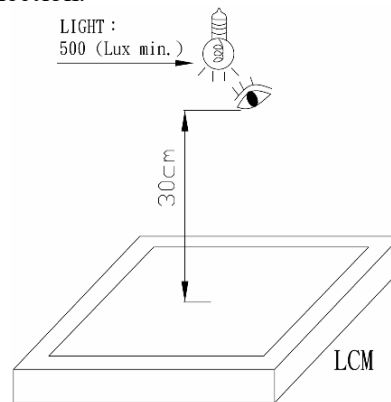
A minor defect is one that does not materially reduce the usability of the unit of product for its intended purpose or is a departure from established standards having no significant bearing on the effective use or operation of the unit.

15.4 Appearance

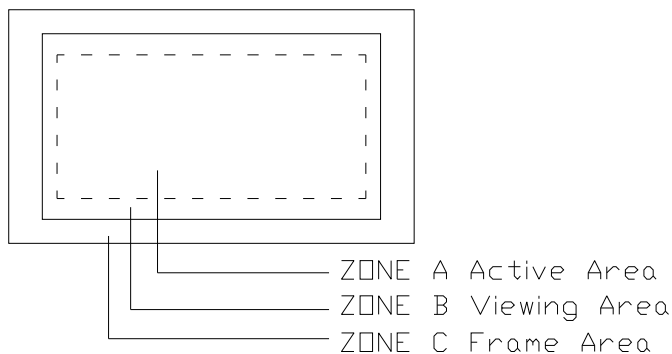
Appearance test is to be conducted by human eyes at approximately 30cm distance from LCD module under the single fluorescent light without reflection.

Condition:

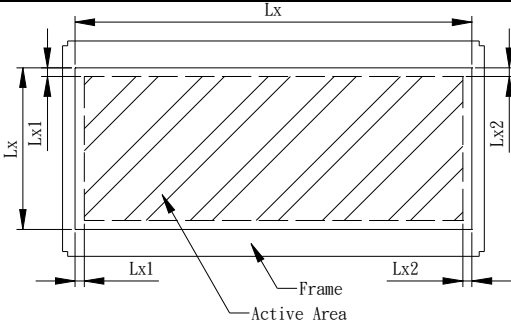
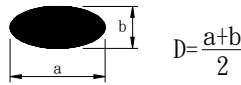
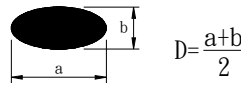
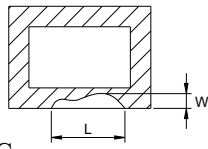
- (a) Illumination: 500 Lux min
- (b) Inspect determination: 30cm
- (c) Inspect direction: above the LCM
- (d) View angle: $\pm 30^{\circ}$



The inspection area of LCD panel shall be within the range of following limits.



15.5 Inspection quality criteria for TFT LCM

ITEM	DESCRIPTION OF DEFECTS	Zone	Acceptable level (%)	
DIMENSION	Refer to individual acceptance specification	ABC	2.5	
SLANT	Viewing Area	Lx1-Lx2	Judgment	
	Lx ≤ 100mm	≤ 0.2mm	ACC	
	100mm < Lx ≤ 150mm	≤ 0.3mm	ACC	
	150mm < Lx ≤ 200mm	≤ 0.4mm	ACC	
	200mm < Lx	≤ 0.5mm	ACC	
			A	2.5
LINE DEFECT ON SURFACE (SCRATCHES, BLACK/WHITE LINE)	(a) W ≤ 0.03mm, disregard (b) L ≤ 2mm & 0.03mm < W ≤ 0.05mm, N ≤ 1 (c) L > 2mm or W > 0.05mm, REJ (e) Distance between 2 lines ≥ 15mm	A	2.5	
SPOT DEFECT ON SURFACE (BLACK/WHITE SPOT)	Average diameter, D (a) D ≤ 0.1mm, disregard (b) 0.1mm < D ≤ 0.2mm, N ≤ 2.ACC (c) 0.2mm < D ≤ 0.25mm, N ≤ 1.ACC (d) D > 0.25mm, REJ (e) Distance between 2 spots ≥ 3.0mm		A	2.5
PROTRUDE DOT/ DENT ON SURFACE	Average diameter D (a) D ≤ 0.2mm, disregard (b) 0.2mm < D ≤ 0.3mm, N ≤ 2.ACC (c) 0.3mm < D ≤ 0.5mm, N ≤ 1.ACC (d) D > 0.5mm, REJ (e) Distance between 2 protrude dot/ dent ≥ 15mm		A	2.5
POLARIZER EDGE	BUBBLES · DENTS · RESIDUAL GLUE · DECKLE EDGE : Active Area outside area don't care.	A,B	2.5	
DAMAGE	(a) Lead side of TFT LCM FPC lead electrical line can't be damage, except dummy electrical line and alignment mark.  (b) Non-lead side of TFT LCM Damage area L ≤ 2.5mm, W ≤ 0.7mm, ACC	A	0.65	

NOTE(1): ACC : Accept
NOTE(2): REJ : Reject

ITEM	DESCRIPTION OF DEFECTS	Zone	Acceptable level (%)		
BRIGHT/ DARK POINT	<table border="1"> <thead> <tr> <th data-bbox="397 325 901 399">Item</th> <th data-bbox="901 325 1128 399">Allow number in Area A</th> </tr> </thead> </table>	Item	Allow number in Area A	A	2.5
	Item	Allow number in Area A			
	(a) Bright point	Single point	2		
		Two adjacent point	0		
		Three adjacent point	0		
		Total point	2		
	(b) Dark point	Single point	3		
		Two adjacent point	0		
	Three adjacent point	0			
	Total point	3			
※ Point : A sub pixel 1R or 1G or 1B ※ The distance of bright or dark point > 5mm		A	2.5		
CHROMA MURA	Not allowed if it can be observed through ND Filter 6%. Refer to individual acceptance limited sample	A	2.5		
COLOR NOT ACCORD	Not allowed if it can be observed through ND Filter 6%. Refer to individual acceptance limited sample	A	2.5		
DISPLAY ABNORMAL	(a) Non display (b) Line defect (c) Response time, contrast ratio, brightness or viewing angle abnormal (d) Water ripple (e) Flicker	A	0.65		

NOTE(1): ACC : Accept

NOTE(2): REJ : Reject