



## ASI-T-240DAKBN/D

Item	Contents	Unit
Size	2.4	inch
Resolution	240(RGB) x 320	/
Interface	CPU 16 bit	/
Technology type	a-Si TFT	/
Pixel pitch	0.153 x 0.153	mm
Pixel Configuration	R.G.B. Vertical Stripe	
Viewing Direction	6 o'clock	
Outline Dimension (W x H x D)	42.72 x 60.35 x 2.90	mm
Active Area	36.72 x 48.96	mm
Display Mode	Transmissive, Normally white	/
Surface Polarizer	Glare type	/
Backlight Type	6 LEDs series	/
Driver IC	ILI9341	



## Record of Revision

Date	Revision No.	Summary
2014-07-04	1.0	Rev 1.0 was issued
2016-09-05	1.1	Add power consumption
2017-03-10	1.2	Add LED backlight Life time
2017-03-16	1.3	Update Outline Drawing
2018-11-23	1.4	Modify the maximum VCC voltage



1. Scope

This data sheet is to introduce the specification of ASI-T-240DAKBN/D, active matrix TFT module. It is composed of a color TFT-LCD panel, driver IC and a backlight unit. The 2.4" display area contains 240(RGB) x320 pixels.

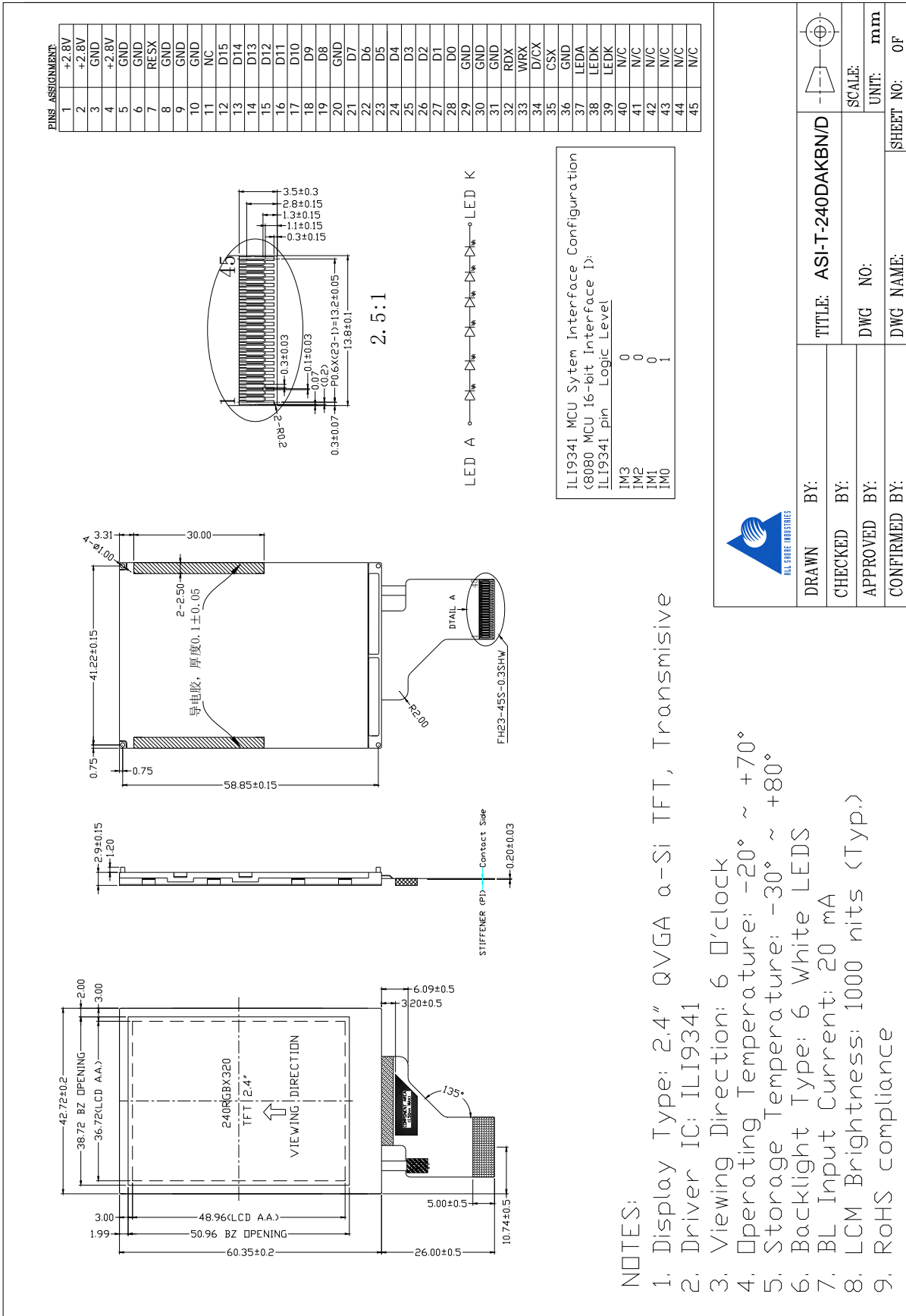
2. Application

Digital equipments which need color display, mobile phone, mobile navigator/video systems.

3. General Information

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### 4. Outline Drawing



## 5. Interface signals

No	Symbol	I/O	Description	Remarks
1, 2	+2.8V	P	Power VCC = +2.8V	
3	GND	P	Power Ground	
4	+2.8V	P	Power VCC = +2.8V	
5~6	GND	P	Power Ground	
7	RESX	I	Reset signal	
8~10	GND	P	Power Ground	
11	NC	-	No connection	
12~19	D15 ~ D8	I/O	Data bus	
20	GND	P	Power Ground	
21~28	D7~D0	I/O	Data bus	
29~31	GND	P	Power Ground	
32	RDX	I	Read signal and MCU read data	
33	WRX	I	Write signal and writes data	
34	D/CX	I	Data or command select	
35	CSX	I	Chip select input pin ( "Low" enable)	
36	GND	P	Power Ground	
37	LEDA	P	Power input for backlight (+)	
38, 39	LEDK	P	Power input for backlight (-)	
40~45	NC	-	No connection	

Note 1: P ---- Power; I ---- Input; O ---- Output.

Note 2: Matching connector P/N: FH23-45S-0.3SHW (Hirose) or Eqv.



6. Absolute maximum Ratings

6.1. Electrical Absolute max. ratings

Parameter	Symbol	MIN	MAX	Unit	Remark
Power Supply Voltage	VCC	-0.3	4.6	V	
Input Signal Voltage	VIN	-0.3	VCC +0.3	V	

6.2. Environment Conditions

Item	Symbol	MIN	MAX	Unit	Remark
Operating Temperature	TOPR	-20	70	°C	
Storage Temperature	TSTG	-30	80	°C	

6.3 LED

Item	Symbol	MIN	MAX	Unit	Remark
Forward current	ILED	-	25	mA	

## 7. Electrical Specifications

### 7.1 Electrical characteristics

GND=0V, Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Analog supply voltage	VCC	--	2.8	3.2	V	
Input Signal Voltage	VIL	GND	--	0.3xVCC	V	
	VIH	0.7xVCC	--	VCC	V	
Output Signal Voltage	VOL	GND	--	0.2xVCC	V	IOL=1.0mA
	VOH	0.8xVCC	--	VCC	V	
Current Consumption	ICC	-	5.5	-	mA	Work on VCC=2.8V
	Isleep	-	0.05	0.1	mA	Sleep mode

### 7.2 LED Backlight

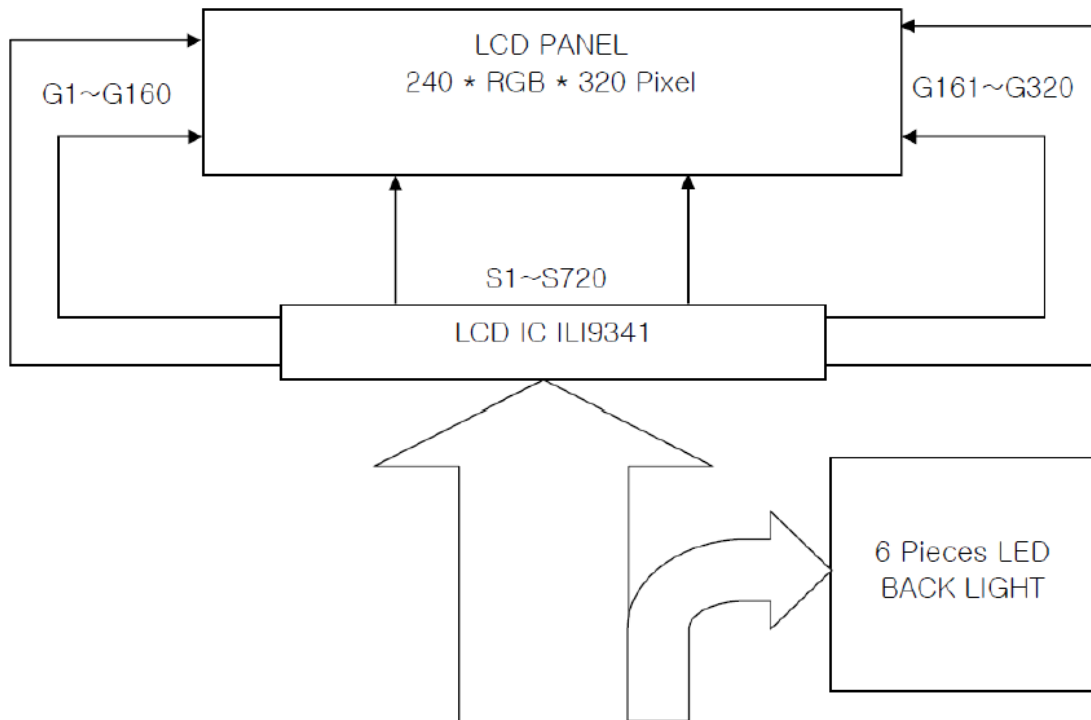
Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	IF	--	20	--	mA	
Forward Voltage	VF	--	19.2	--	V	
Power Consumption	PLED	--	384	--	mW	For reference
Life time	-	-	20000	-	Hr	-

Note: The figure below shows the connection of backlight LED.



### 7.3 Schematic of LCD module system

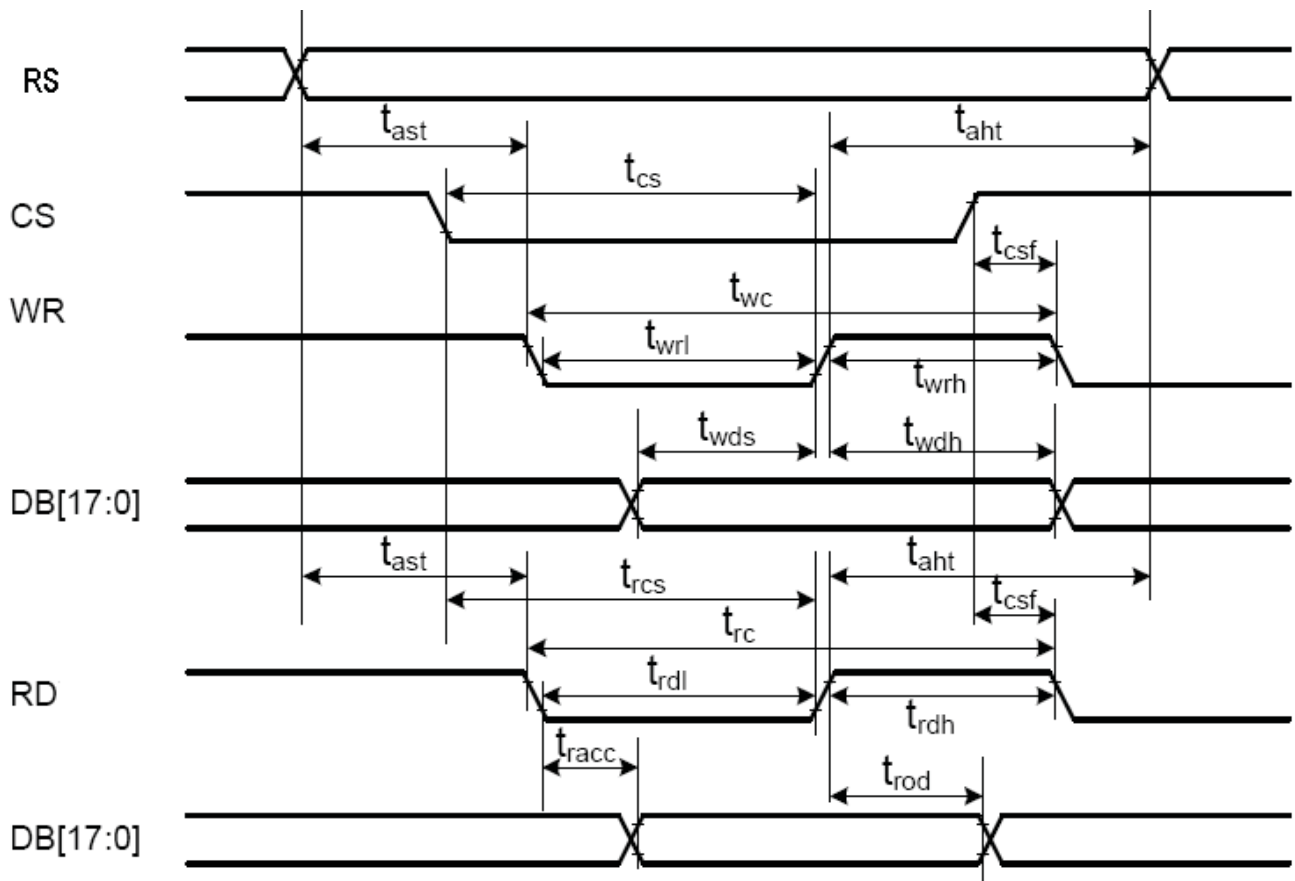




## 8 Command/AC Timing

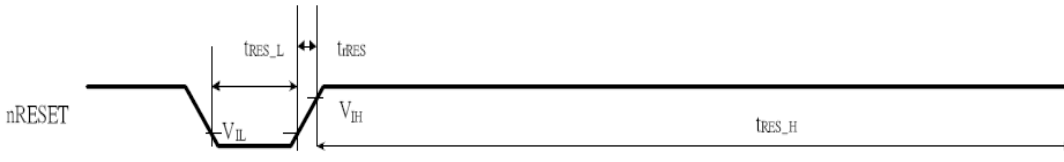
### 8.1 CPU Interface Timing

Signal	Symbol	Parameter	Min	Max	Unit
RS	t <sub>ast</sub>	Address setup time	0	-	ns
	t <sub>aht</sub>	Address hold time(Write/Read)	10	-	ns
CS	t <sub>cs</sub>	Chip Select setup time(Write)	15	-	ns
	t <sub>r<sub>cs</sub></sub>	Chip Select setup time(Read)	45	-	ns
	t <sub>csf</sub>	Chip Select Wait time(Write/Read)	10	-	ns
WR	t <sub>wc</sub>	Write cycle	66	-	ns
	t <sub>wrh</sub>	Write Control pulse H duration	15	-	ns
	t <sub>wrl</sub>	Write Control pulse L duration	15	-	ns
RD	t <sub>rc</sub>	Read cycle	160	-	ns
	t <sub>rdh</sub>	Read Control pulse H duration	90	-	ns
	t <sub>rdl</sub>	Read Control pulse L duration	45	-	ns
DB[17:0]	t <sub>wds</sub>	Write data setup time	10	-	ns
DB[15:0]	t <sub>wdh</sub>	Write data hold time	10	-	ns
DB[8:0]	t <sub>racc</sub>	Read access time	-	340	ns
DB[7:0]	t <sub>rod</sub>	Read output disable time	20	80	ns



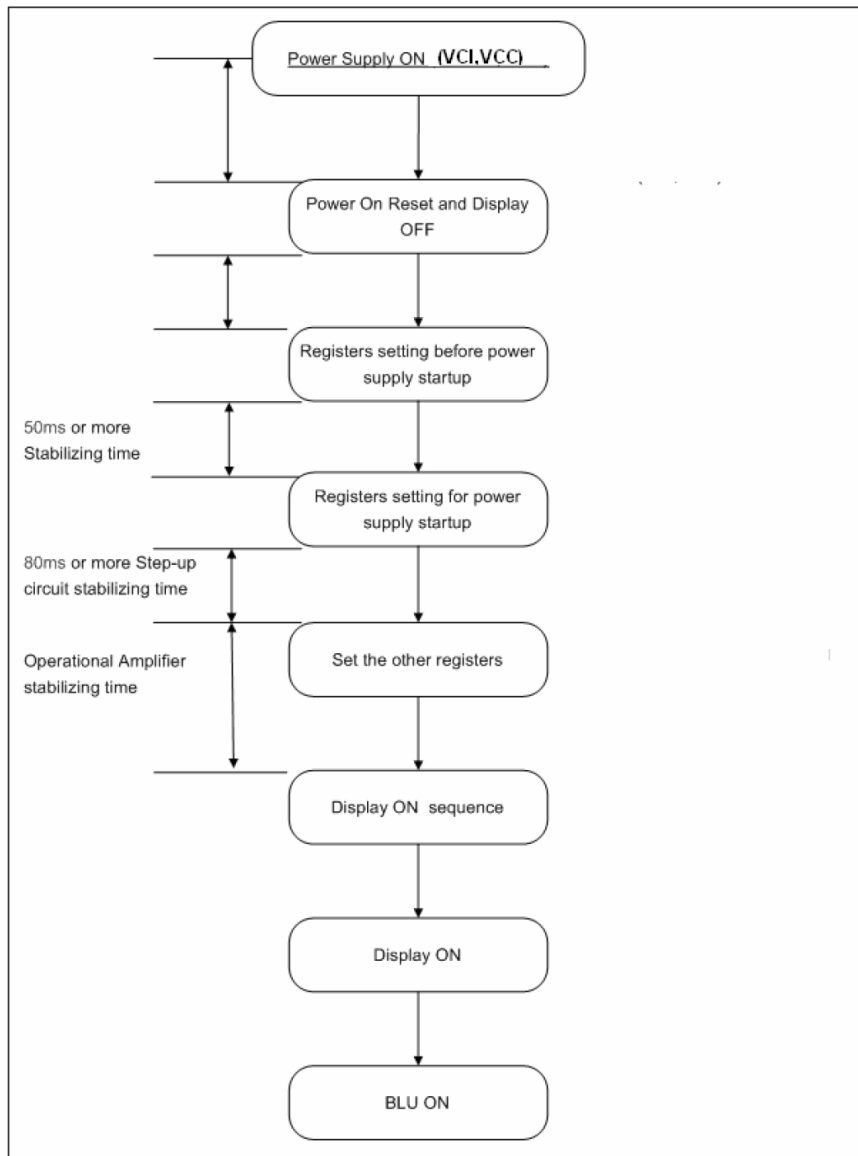
### 8.2 Reset Timing Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
RESET low-level width	tRES_L	1	-	-	ms
RESET rise time	trRES	-	-	10	s
Reset high-level width	tRES_H	50	-	-	ms



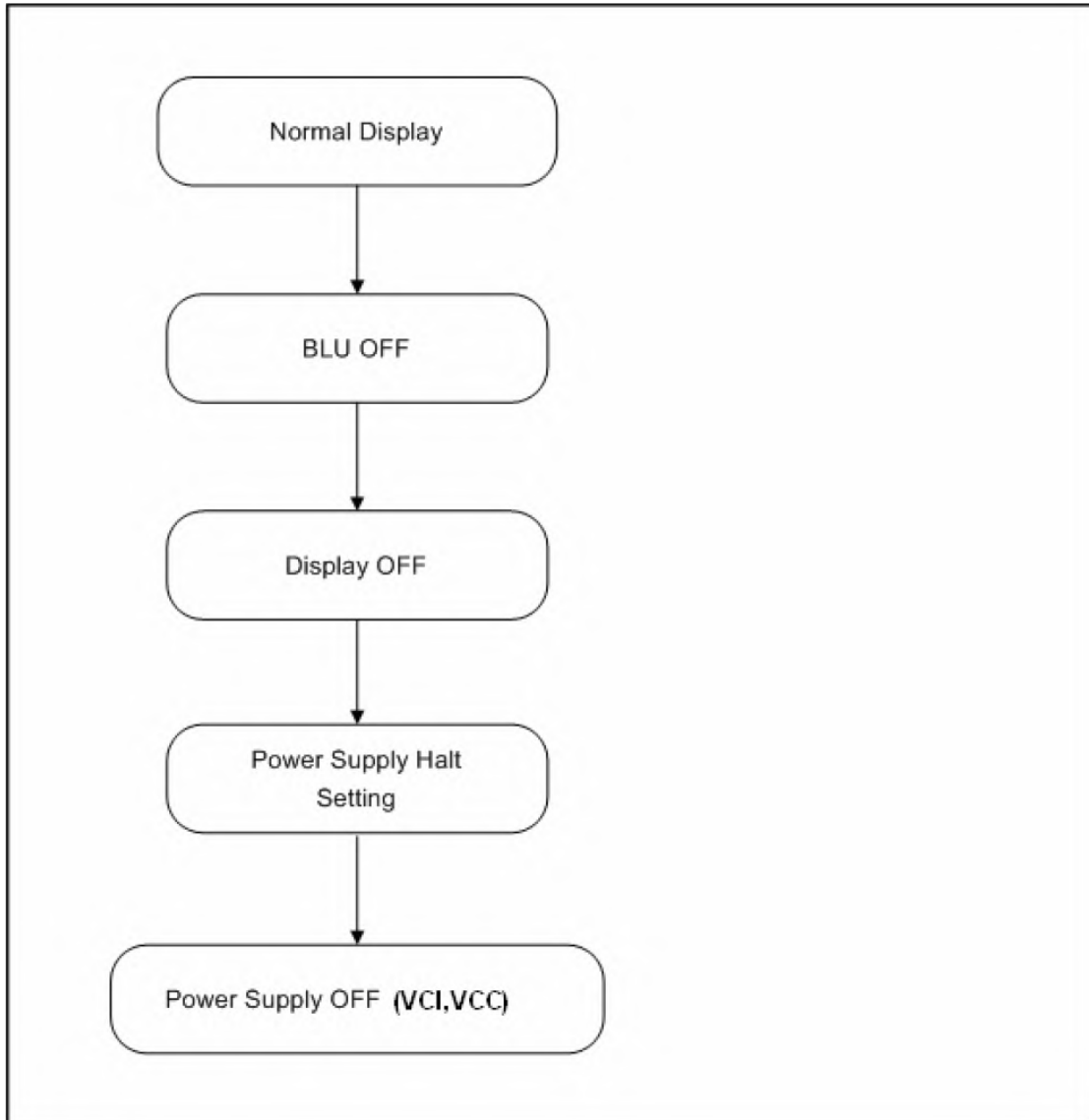
### 8.3 Power ON/Off Sequence

Power on





Power off



## 9 Optical Specification

Ta=25°C

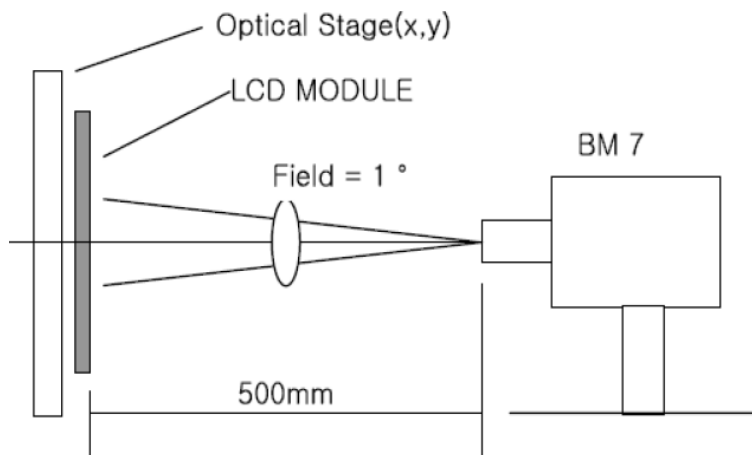
Item	Symbol	Condition	Min	Typ.	Max.	Unit	Remark
Contrast Ratio	CR	$\theta=0^\circ$	300	350	-		Note1 Note3
Response Time	Tr + Tf	25°C	-	20	30	ms	Note1 Note4
View Angles	$\theta T$	CR $\geq$ 10	-	50	-	Degree	Note 2
	$\theta B$		-	30	-		
	$\theta L$		-	45	-		
	$\theta R$		-	45	-		
NTSC	S		-	50	-	%	Note 5
Luminance	L		-	1000	-	cd/m <sup>2</sup>	Note1 Note6
Uniformity	U		-	80	-	%	Note1 Note7

Test condition: IF= 20mA (LED current), VF=19.2V, the ambient temperature is 25°C.

### Note 1: Definition of optical measurement system.

Temperature = 25°C(±3°C)

LED back-light: ON, Environment brightness < 150 lx

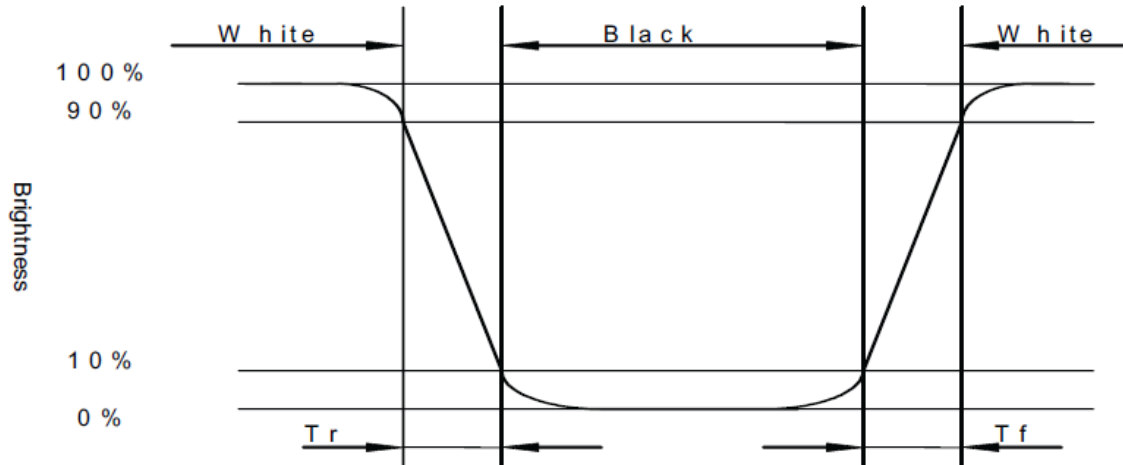


### Note 2: Contrast ratio is defined as follow:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

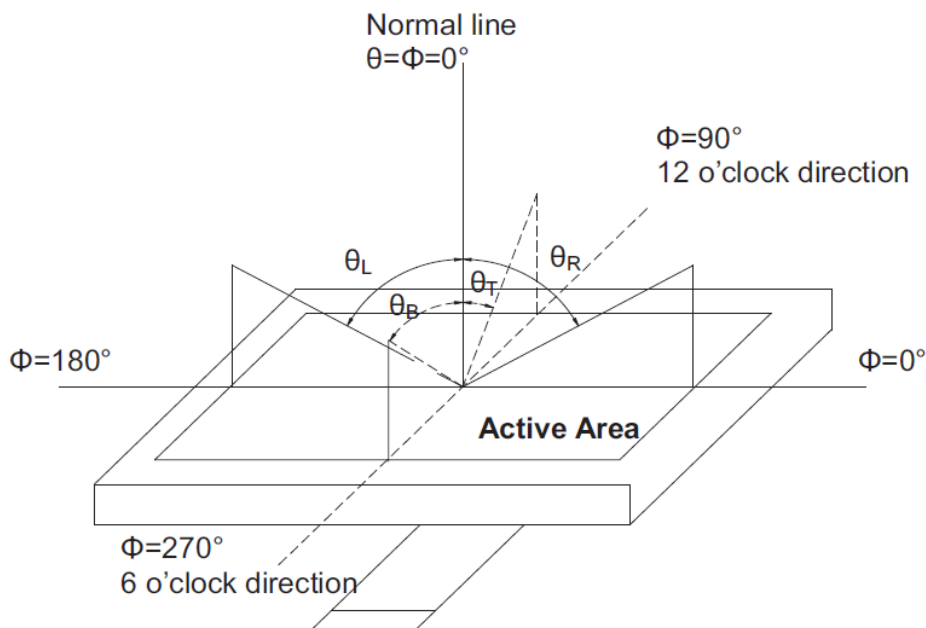
Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise Time,  $T_r$ ) and from white to black (Decay Time,  $T_f$ ).



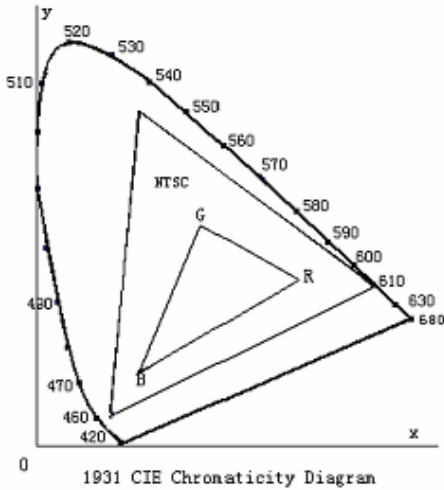
Note 4: Viewing angle range is defined as follow:

Viewing angle is measured at the center point of the LCD.



Note 5: Color chromaticity is defined as follow: (CIE1931)

Color coordinates measured at center point of LCD.



$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels “White” at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Uniformity (U)} = \frac{\text{Minimum Luminance( brightness ) in 9 points}}{\text{Maximum Luminance( brightness ) in 9 points}}$$

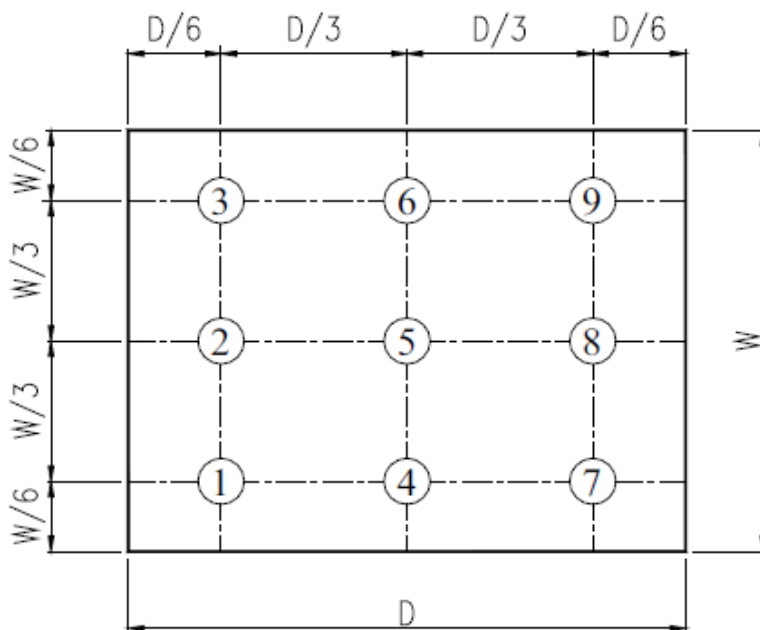


Fig. 2 Definition of uniformity

10 Environmental / Reliability Tests

No	Test Item	Condition	Judgment criteria
1	High Temp Operation	Ts=+70°C , 120hrs	Per table in below
2	Low Temp Operation	Ta=- 20°C , 120hrs	Per table in below
3	High Temp Storage	Ta=+80°C , 120hrs	Per table in below
4	Low Temp Storage	Ta=-30°C , 120hrs	Per table in below
5	High Temp & High Humidity Storage	Ta=+60°C , 90% RH 120 hours	Per table in below (polarizer discoloration is excluded)
6	Thermal Shock (Non-operation)	-30°C 30 min~+80°C 30 min, Change time:5min, 10 Cycles	Per table in below
7	ESD (Operation)	C=150pF, R=330Ω , 5points/panel Air:±8KV, 5times; Contact:±4KV, 5 times;	Per table in below
8	Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z.	Per table in below
9	Shock (Non-operation)	60G 6ms, ±X,±Y,±Z 3times, for each direction	Per table in below
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	Per table in below

INSPECTION	CRITERION(after test)
Appearance	No Crack on the FPC, on the LCD Panel
Alignment of LCD Panel	No Bubbles in the LCD Panel No other Defects of Alignment in Active area
Electrical current	Within device specifications
Function / Display	No Broken Circuit, No Short Circuit or No Black line No Other Defects of Display

## 11 Precautions for Use of LCD Modules

### 11.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

### 11.2 Handling

A. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.

B. Do not handle the product by holding the flexible pattern portion in order to assure the reliability

C. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.

D. Provide a space so that the panel does not come into contact with other components.

E. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.

F. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.

G. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.

H. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

### 11.3 Static Electricity

A. Ground soldering iron tips, tools and testers when they are in operation.

B. Ground your body when handling the products.

C. Power on the LCD module before applying the voltage to the input terminals.

D. Do not apply voltage which exceeds the absolute maximum rating.

E. Store the products in an anti-electrostatic bag or container.

### 11.4 Storage

A. Store the products in a dark place at  $+25^{\circ}\text{C} \pm 10^{\circ}\text{C}$  with low humidity (40% RH to 60% RH). Don't expose to sunlight or fluorescent light.

B. Storage in a clean environment, free from dust, active gas, and solvent.

### 11.5 Cleaning

A. Do not wipe the touch panel with dry cloth, as it may cause scratch.

B. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

### 11.6 Cautions for installing and assembling

Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.

